

- b. force
- c. impulse
- d. pressure

Q11. Which of the following is a unit that of force?

- a. N m
- b. m N
- c. n m
- d. N s

Q12. Major contribution of J.J.Thomson

- a. Generation of electromagnetic waves
- b. Laws of electromagnetic induction
- c. X-rays
- d. Discovery of electron

Q13. The moon is observed from two diametrically opposite points A and B on Earth. The angle θ subtended at the moon by the two directions of observation is $1^\circ 54'$. Given the diameter of the Earth to be about 1.276×10^7 m, the distance of the moon from the earth is

- a. 3.84×10^8 m
- b. 3.84×10^9 m
- c. 3.84×10^7 m
- d. 3.84×10^{10} m

Q14. A body starts from rest and travels on straight path for t second with uniform acceleration of 2 m/s^2 . If the displacement made by it is 25 m, the time of travel t is

- a. 4 s
- b. 3 s
- c. 6 s
- d. 5 s

Q15. Which of the following physical quantities has unit Watt hour?

- a. Energy
- b. Force
- c. Momentum
- d. Pressure

Q16. A boy starts from a point A, travels on straight line to a point B at a distance of 3 km from A and returns to A. If he takes 2 hours to do so, his average speed is

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web vhdl stands for very high speed integration circuit hdl hardware description language it is an ieee institute of electrical and electronics engineers standard hardware description language that is used to describe and simulate the behavior of complex digital circuits

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web as an alternative you can also write your simulation stimulus using generic vhdl code called a testbench the testbench codes gets compiled along with the design so that the stimulus is also in the compiled simulation model ieee came along and created additional libraries that are supported by almost all vhdl compilers specific silicon

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web for sample syntax and a list of vhdl statements supported by the vhdl synthesizer see appendix a quick reference for a list of exceptions and constraints on the vhdl synthesizer s support of vhdl see appendix b limitations this chapter shows you the structure of a vhdl design and then

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web feb 16 2023 by using the coding style above there is no question about which version of bottom is wanted the version in the library my lib1 is the correct one care should be taken when choosing library names for vhd1 files while a library called work is a common library name for many projects the tool will handle it a little differently than

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web high level synthesisable vhd1 hvhdl is a set of coding patterns for standard vhd1 which are designed to greatly increase abstraction level of vhd1 source code using standard synthesis tools the coding patterns are designed to support incremental design testing and development of the vhd1 source code

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web dec 29 2017 vhd1 uses a similar description however it has its own syntax for example it uses the following lines of code to describe the input and output ports of this circuit 1 entity circuit 1 is 2 port a in std logic 3 b in std logic 4 out1 out std logic 5 end circuit 1

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web basic logic gates esd chapter 2 figure 2 3 every vhd1 design description consists of at least one entity architecture pair or one entity with multiple architectures the entity section of the hdl design is used to declare the i o ports of the circuit while the description code resides within architecture portion

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web quartus and vhd1 code in this section you should learn how to implement a vhd1 code for single cycle data path before we go any further let s define some of the terms that we use throughout the book entity all designs are expressed in terms of entities an entity is the most basic building block in a design