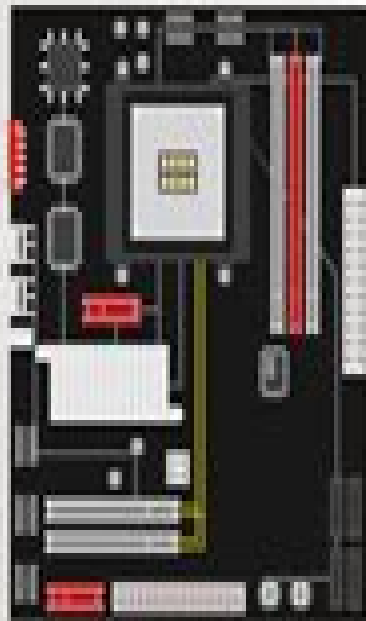
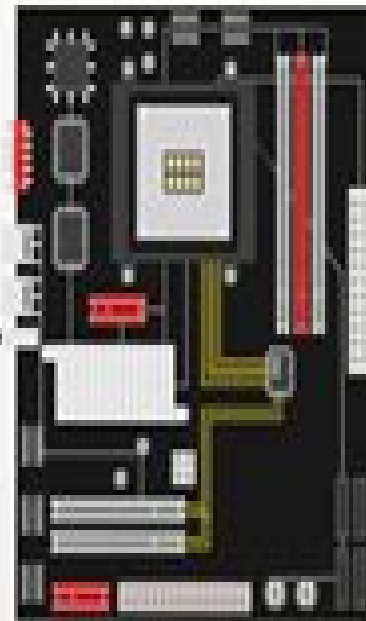


Processor allocated lanes



- Direct Connection to CPU
- More bandwidth
- Not bottlenecked by Chipset

Chipset allocated lanes



- Direct Connection to Chipset
- Limited bandwidth
- Bottlenecked by Chipset - CPU DMA

Pci Chipset User Guide

Yi-Tong Ma



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