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Primož Gabrijelčič



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SystemC Wolfgang Müller, Wolfgang Rosenstiel, Jürgen Ruf, 2007-05-08 SystemC has received a wide acceptance by users and tool vendors as the next generation system description language in order to deal with higher levels of abstraction for complex SoC designs SystemC Methodologies and Applications gives a comprehensive survey on the state of the art of SystemC in industry and research Organised into 11 self contained chapters selected SystemC experts present their approaches in the domains of modelling analysis synthesis Their contributions range from mixed signal and discrete system to embedded software The chapters give a broad overview of recent advances in SystemC methodologies and applications and are mainly based on presentations given at the European SystemC User Group meetings *Mastering Delphi Programming: A Complete Reference Guide* Primož Gabrijelčič, 2019-11-26 Use structural behavioral and concurrent patterns in Delphi to skillfully develop applications Key Features Delve into the core patterns and components of Delphi to enhance your application s design Learn how to select the right patterns to improve your program s efficiency and productivity Discover how parallel programming and memory management can optimize your code Book Description Delphi is a cross platform Integrated Development Environment IDE that supports rapid application development for most operating systems including Microsoft Windows iOS and now Linux with RAD Studio 10.2 If you know how to use the features of Delphi you can easily create scalable applications in no time This Learning Path begins by explaining how to find performance bottlenecks and apply the correct algorithm to fix them You ll brush up on tricks techniques and best practices to solve common design and architectural challenges Then you ll see how to leverage external libraries to write better performing programs You ll also learn about the eight most important patterns that ll enable you to develop and improve the interface between items and harmonize shared memories within threads As you progress you ll also delve into improving the performance of your code and mastering cross platform RTL improvements By the end of this Learning Path you ll be able to address common design problems and feel confident while building scalable projects This Learning Path includes content from the following Packt products Delphi High Performance by Primo Gabrijel i Hands On Design Patterns with Delphi by Primo Gabrijel i What you will learn Understand parallel programming and work with the various tools included with Delphi Explore memory managers and their implementation Leverage external libraries to write better performing programs Keep up to date with the latest additions and design techniques in Delphi Get to grips with various modern multithreading approaches Break a design problem down into its component parts Who this book is for This Learning Path is for intermediate level Delphi programmers who want to build robust applications using Delphi features Prior knowledge of Delphi is assumed The Compiler Design Handbook Y.N. Srikant, Priti Shankar, 2018-10-03 Today s embedded devices and sensor networks are becoming more and more sophisticated requiring more efficient and highly flexible compilers Engineers are discovering that many of the compilers in use today are ill suited to meet the demands of more advanced computer

architectures Updated to include the latest techniques The Compiler Design Handbook Second Edition offers a unique opportunity for designers and researchers to update their knowledge refine their skills and prepare for emerging innovations The completely revised handbook includes 14 new chapters addressing topics such as worst case execution time estimation garbage collection and energy aware compilation The editors take special care to consider the growing proliferation of embedded devices as well as the need for efficient techniques to debug faulty code New contributors provide additional insight to chapters on register allocation software pipelining instruction scheduling and type systems Written by top researchers and designers from around the world The Compiler Design Handbook Second Edition gives designers the opportunity to incorporate and develop innovative techniques for optimization and code generation

Transaction-Level Power Modeling Amr Baher Darwish, Magdy Ali El-Moursy, Mohamed Amin Dessouky, 2019-08-01 This book describes for readers a methodology for dynamic power estimation using Transaction Level Modeling TLM The methodology exploits the existing tools for RTL simulation design synthesis and SystemC prototyping to provide fast and accurate power estimation using Transaction Level Power Modeling TLPM Readers will benefit from this innovative way of evaluating power on a high level of abstraction at an early stage of the product life cycle decreasing the number of the expensive design iterations

Digital Logic Design Using Verilog Vaibbhav Taraate, 2021-10-31 This second edition focuses on the thought process of digital design and implementation in the context of VLSI and system design It covers the Verilog 2001 and Verilog 2005 RTL design styles constructs and the optimization at the RTL and synthesis level The book also covers the logic synthesis low power multiple clock domain design concepts and design performance improvement techniques The book includes 250 design examples illustrations and 100 exercise questions This volume can be used as a core or supplementary text in undergraduate courses on logic design and as a text for professional and vocational coursework In addition it will be a hands on professional reference and a self study aid for hobbyists

Emerging Memory and Computing Devices in the Era of Intelligent Machines Pedram Khalili Amiri, 2020-04-16 Computing systems are undergoing a transformation from logic centric towards memory centric architectures where overall performance and energy efficiency at the system level are determined by the density performance functionality and efficiency of the memory rather than the logic sub system This is driven by the requirements of data intensive applications in artificial intelligence autonomous systems and edge computing We are at an exciting time in the semiconductor industry where several innovative device and technology concepts are being developed to respond to these demands and capture shares of the fast growing market for AI related hardware This special issue is devoted to highlighting discussing and presenting the latest advancements in this area drawing on the best work on emerging memory devices including magnetic resistive phase change and other types of memory The special issue is interested in work that presents concepts ideas and recent progress ranging from materials to memory devices physics of switching mechanisms circuits and system applications as well as progress in modeling and design tools Contributions that

bridge across several of these layers are especially encouraged

Domain Specific High-Level Synthesis for

Cryptographic Workloads Ayesha Khalid, Goutam Paul, Anupam Chattopadhyay, 2019-03-28 This book offers an in depth study of the design and challenges addressed by a high level synthesis tool targeting a specific class of cryptographic kernels i.e symmetric key cryptography With the aid of detailed case studies it also discusses optimization strategies that cannot be automatically undertaken by CRYKET Cryptographic kernels toolkit The dynamic nature of cryptography where newer cryptographic functions and attacks frequently surface means that such a tool can help cryptographers expedite the very large scale integration VLSI design cycle by rapidly exploring various design alternatives before reaching an optimal design option Features include flexibility in cryptographic processors to support emerging cryptanalytic schemes area efficient multinational designs supporting various cryptographic functions and design scalability on modern graphics processing units GPUs These case studies serve as a guide to cryptographers exploring the design of efficient cryptographic implementations

Algorithms and VLSI Implementations of MIMO Detection Ibrahim A. Bello, Basel Halak, 2022-07-22 This book provides a detailed overview of detection algorithms for multiple input multiple output MIMO communications systems focusing on their hardware realisation The book begins by analysing the maximum likelihood detector which provides the optimal bit error rate performance in an uncoded communications system However the maximum likelihood detector experiences a high complexity that scales exponentially with the number of antennas which makes it impractical for real time communications systems The authors proceed to discuss lower complexity detection algorithms such as zero forcing sphere decoding and the K best algorithm with the aid of detailed algorithmic analysis and several MATLAB code examples Furthermore different design examples of MIMO detection algorithms and their hardware implementation results are presented and discussed Finally an ASIC design flow for implementing MIMO detection algorithms in hardware is provided including the system simulation and modelling steps and register transfer level modelling using hardware description languages Provides an overview of MIMO detection algorithms and discusses their corresponding hardware implementations in detail Highlights architectural considerations of MIMO detectors in achieving low power consumption and high throughput Discusses design tradeoffs that will guide readers efforts when implementing MIMO algorithms in hardware Describes a broad range of implementations of different MIMO detectors enabling readers to make informed design decisions based on their application requirements

The Computer Engineering Handbook Vojin G. Oklobdzija, 2001-12-26 There is arguably no field in greater need of a comprehensive handbook than computer engineering The unparalleled rate of technological advancement the explosion of computer applications and the now in progress migration to a wireless world have made it difficult for engineers to keep up with all the developments in specialties outside their own References published only a few years ago are now sorely out of date The Computer Engineering Handbook changes all of that Under the leadership of Vojin Oklobdzija and a stellar editorial board some of the industry's foremost experts have joined forces to

create what promises to be the definitive resource for computer design and engineering. Instead of focusing on basic introductory material, it forms a comprehensive state-of-the-art review of the field's most recent achievements, outstanding issues, and future directions. The world of computer engineering is vast and evolving so rapidly that what is cutting edge today may be obsolete in a few months. While exploring the new developments, trends, and future directions of the field, the Computer Engineering Handbook captures what is fundamental and of lasting value. *Emerging Directions in Embedded and Ubiquitous Computing* Xiaobo Zhou, Oleg Sokolsky, Lu Yan, Eun-Sun Jung, Zili Shao, Yi Mu, Dong-Chun Lee, Daeyoung Kim, Young-Sik Jeong, Cheng-Zhong Xu, 2006-08-29. Here are the refereed proceedings of the EUC 2006 workshops held in conjunction with the IFIP International Conference on Embedded and Ubiquitous Computing in Seoul, Korea, August 2006. The book presents 102 revised papers spanning six workshops: network-centric ubiquitous systems (NCUS 2006), security in ubiquitous computing systems (SecUbiq 2006), RFID and ubiquitous sensor networks (USN 2006), trustworthiness, reliability, and services in ubiquitous and sensor networks (TRUST 2006), embedded software optimization (ESO 2006), and multimedia solution and assurance in ubiquitous information systems (MSA 2006).

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