# VEE UNITS 1 + 2



# **Nelson Vce Psychology Student Activity Manual**

**Lingjun Ying** 

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web step 1 design inputs design inputs are either defined by the end application or selected by the designer these parameters include but are not limited to the input and output voltages power ripple factor and operation mode table 1 shows a summary of the design inputs for the circuit discussed in this article

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web this section introduces flyback circuit design with details on transformer turns ratio magnetizing inductance calculation mosfet diode voltage stress flyback small signal modeling and loop compensation

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web figure 1 flyback converter schematic flyback converter has two signal semi periods ton and toff which are named after and controlled by the mosfet's switching states during ton the mosfet is in the on state and current flows from the input through the primary inductor to linearly charge the coupled inductor

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web flyback converter circuit some designers do not allow for avalanche operation instead a voltage derating is maintained between rated bydss and vdd typically 90 or less in such instances however it is not uncommon that greater than planned for voltage spikes can occur so even the best designs may encounter an infrequent avalanche event

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