



Samsung Series 9 User Manual

Jung Han Choi, Krzysztof Iniewski



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High-Speed and Lower Power Technologies Jung Han Choi, Krzysztof Iniewski, 2018-09-03 This book explores up to date research trends and achievements on low power and high speed technologies in both electronics and optics It offers unique insight into low power and high speed approaches ranging from devices ICs sub systems and networks that can be exploited for future mobile devices 5G networks Internet of Things IoT and data centers It collects heterogeneous topics in place to catch and predict future research directions of devices circuits subsystems and networks for low power and higher speed technologies Even it handles about artificial intelligence AI showing examples how AI technology can be combined with concurrent electronics Written by top international experts in both industry and academia the book discusses new devices such as Si on chip laser interconnections using graphenes machine learning combined with CMOS technology progresses of SiGe devices for higher speed electronics for optic co design low power and high speed circuits for optical interconnect low power network on chip NoC router X ray quantum counting and a design of low power power amplifiers Covers modern high speed and low power electronics and photonics Discusses novel nano devices electronics photonic sub systems for high speed and low power systems and many other emerging technologies like Si photonic technology Si on chip laser low power driver for optic device and network on chip router Includes practical applications and recent results with respect to emerging low power systems Addresses the future perspective of silicon photonics as a low power interconnections and communication applications

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