# Schema User Guide

This is the administrative office that should be contacted to obtain additional information about the FinCEN SAR. There must be only one occurrence of this party for each FinCEN SAR Activity in the batch. This party corresponds with Part IV (Items 93-94) of the FinCEN SAR.

#### LE Contact Agency (if any)

This is the law enforcement agency (if any) that has been informed of the suspicious activity. There may only be one occurrence of this party for each FinCEN SAR Activity in the batch. This party corresponds with Part IV (Item 89) of the FinCEN SAR.

#### LE Contact Name (if any).

This is the person at law enforcement agency (if any) contacted regarding the suspicious activity. There may only be one occurrence of this party for each FinCEN SAR Activity in the batch. This party corresponds with Part IV (Items 90-92) of the FinCEN SAR.

#### Financial Institution Where Activity Occurred (Required)

This is the financial institution where the suspicious activity occurred. There must be <u>at least one</u> occurrence (not to exceed 99) of this party for each FinCEN SAR Activity in the batch. This party corresponds with Part III (Items 51-67, 69a). If a branch location associated with the financial institution where activity occurred is also involved, information about the branch should be recorded in a separate party element under the parent element «PartyAssociation» (see PartyAssociation below).

#### Branch Where Activity Occurred (if any)

This is the branch location where activity occurred. A branch is a location (such as an office or ATM) owned by the financial institution but located separately from the financial institution's headquarters. Generally, a branch operates under the same TIN as the financial institution. No more than 99 branches may be recorded for each FinCEN SAR in the batch. This party corresponds with Part III (items 68-74) of the FinCEN SAR.

#### Subject (Required)

This is the subject involved in the suspicious activity. There must be <u>at least one</u> occurrence (not to exceed 999) of this party for each FinCEN SAR Activity in the batch. This party corresponds with Part I (Items 3-28) of the FinCEN SAR. Persons who are victims of the suspicious activity are not subjects and should not be recorded for this party. Victim information, if necessary for a complete description of the suspicious activity, should be recorded in the narrative. If the subject is or all subjects are unknown, record a single party for the subject with "all critical subject information unavailable" indicated and record the nature of the unknown subject(s) in the narrative. If the suspicious activity involves known and unknown subjects, record a separate party for each known subject and record the nature of the unknown subjects) in the narrative (when there are known subjects involved, do not include a party element for unknown subjects.):

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**Ensheng Dong** 

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is to introduce you to the exciting and challenging field of digital electronics I want to develop your desire and ability to

understand how digital circuits work After reading this book you should be able to do some or all of the following You will understand what TTL and CMOS mean and appreciate their main differences You should know what the five main logic gates are and their respective symbols and Boolean expressions You should know the basics of Boolean algebra and use it to simplify logic expressions and circuits You should know what Karnaugh maps are and how to use them to simplify logic circuits and expressions You should know how to implement the 1st and 2nd canonical formats for Karnaugh maps You will know how the JK flip flop works and how it was born out of the SR latch You should be able to use the JK flip flop and the D type latch to create a series of counters and different shift registers such as SIPO SISO PIPO and PISO You should understand the difference between sequential and combinational logic You should be able to use a range of design techniques that is state diagrams transition tables etc You should be able to create a range of combinational logic circuits such as half and full adders binary subtractors multiplexers etc You should understand how the 555 timer IC works and how to configure it in a range of different applications such as the monostable the astable and PWM You should be able to design a range of logic circuits You should be able to use the ECAD software TINA 12 Trainee's Guide for Electronics Technicians, Class C, AN/SRC-20, AN/SRC-21, Radio Sets United States. Bureau of Naval Personnel, 1967

Organizational Maintenance Manual for Gun, Air Defense Artillery, Self-propelled, 20-mm, M163A1, Cannon M168, Mount M157A1, Sight M61, and Radar AN/VPS-2, (NSN 2350-01-017-2113). ,1989 **Integrated Power** Electronic Converters and Digital Control Ali Emadi, Alireza Khaligh, Zhong Nie, Young Joo Lee, 2017-12-19 Because of the demand for higher efficiencies smaller output ripple and smaller converter size for modern power electronic systems integrated power electronic converters could soon replace conventional switched mode power supplies Synthesized integrated converters and related digital control techniques address problems related to cost space flexibility energy efficiency and voltage regulation the key factors in digital power management and implementation Meeting the needs of professionals working in power electronics as well as advanced engineering students Integrated Power Electronic Converters and Digital Control explores the many benefits associated with integrated converters This informative text details boost type buck type and buck boost type integrated topologies as well as other integrated structures It discusses concepts behind their operation as well specific applications Topics discussed include Isolated DC DC converters such as flyback forward push pull full bridge and half bridge Power factor correction and its application Definition of the integrated switched mode power supplies Steady state analysis of the boost integrated flyback rectifier energy storage converter Dynamic analysis of the buck integrated forward converter Digital control based on the use of digital signal processors DSPs With innovations in digital control becoming ever more pervasive system designers continue to introduce products that integrate digital power management and control integrated circuit solutions both hybrid and pure digital This detailed assessment of the latest advances in the field will help anyone working in power electronics and related industries stay ahead of the curve **Applied**  Analog Electronics: A First Course In Electronics Kevin Karplus, 2023-06-06 This textbook is for a first course on electronics It assumes no prior electronics experience but does assume that students have had calculus 1 single variable differential calculus and high school physics A key idea of the course is that students need a lot of design experience and hands on work rather than a lot of theory The course is centered around the labs which are a mix of design labs and measurement modeling labs This unique volume takes students from knowing no electronics to being able to design and build amplifier and filter circuits for connecting sensors to microcontrollers within 20 weeks Students design a digital thermometer a blood pressure meter an optical pulse monitor an EKG an audio preamplifier and a class D power amplifier They also learn how to measure and characterize components including impedance spectroscopy of a loudspeaker and of electrochemical electrodes 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Documents: Systems and Principles Peter King, Ethan V. Munson, 2004-02-12 This volume contains the proceedings of two recent conferences in the eld of electronic publishing and digital documents DDEP 2000 the 8th International Conference on Digital Documents and Electronic Publishing the successor conference to the EP conference series and PODDP 2000 the 5th International Workshop on the Principles of Digital Document Processing Both conferences were held at the Technische Universit at Munc hen Munich Germany in September 2000 DDEP 2000 was the eighth in a biennial series of international conferences organized to promote the exchange of novel ideas concerning the computer p duction manipulation and dissemination of documents This conference series has attempted to re ect the evolving nature and usage of documents by treating digital documents and electronic publishing as a broad topic covering many pects These aspects have included document models document representation and document dissemination dynamic and hyper documents document ana sis and management and wide ranging applications The papers presented at DDEP 2000 and in this volume re ect this broad view and cover such diverse topicsashypermediastructure and design multimedia authoring techniques and systems document structure inference typography document management and adaptation document collections and Petri nets All papers were refereed by an international program committee

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