

Viva Questions and Answers

ADE LAB

(15CSL37)

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2015-CBES - Syllabus

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Patil

1) Classify basic gates and Universal gates.

Ans: OR, AND and NOT are basic gates

NAND and NOR gates are Universal gates.

2) Why NAND and NOR gates are called as Universal gates.

Ans: We can realize all Basic gates using NAND and NOR gates.

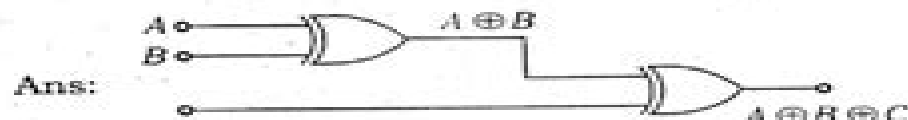
3) Application of X-OR gate.

Ans: Adders, Subtractors, parity checker/Generator, code converters, etc.

4) Application of X-NOR gate.

Ans: Digital comparator

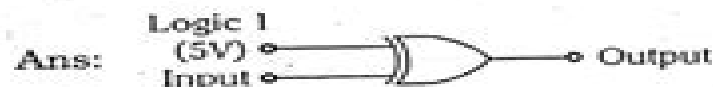
5) Realize 3 input X-OR gate using 2 input X-OR gates.



6) In 74LS00, IC, LS stands for What?

Ans: LS → low power schottky.

6a) Realize Inverter gate from X-OR gate.



7) Give typical logic levels for TTL gates.

Ans: $V_{IL} = 0.8 \text{ V}$ $V_{OL} = 0.4 \text{ V}$

$V_{IH} = 2 \text{ V}$ $V_{OH} = 2.4 \text{ V}$

8) What is meant by fan-in of a gate?

Ans: No. of inputs = fan-in

9) What is fan-out of a gate?

Ans: It is the maximum number of inputs of the same IC family that the gate can drive while maintaining its output level within the specified limit.

10) What is + ve logic and - ve logic?

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SA Adler



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