

PCB STACK UP

LAYER 1: TOP
LAYER 2: GND
LAYER 3: IN1
LAYER 4: IN2
LAYER 5: VCC
LAYER 6: BOT

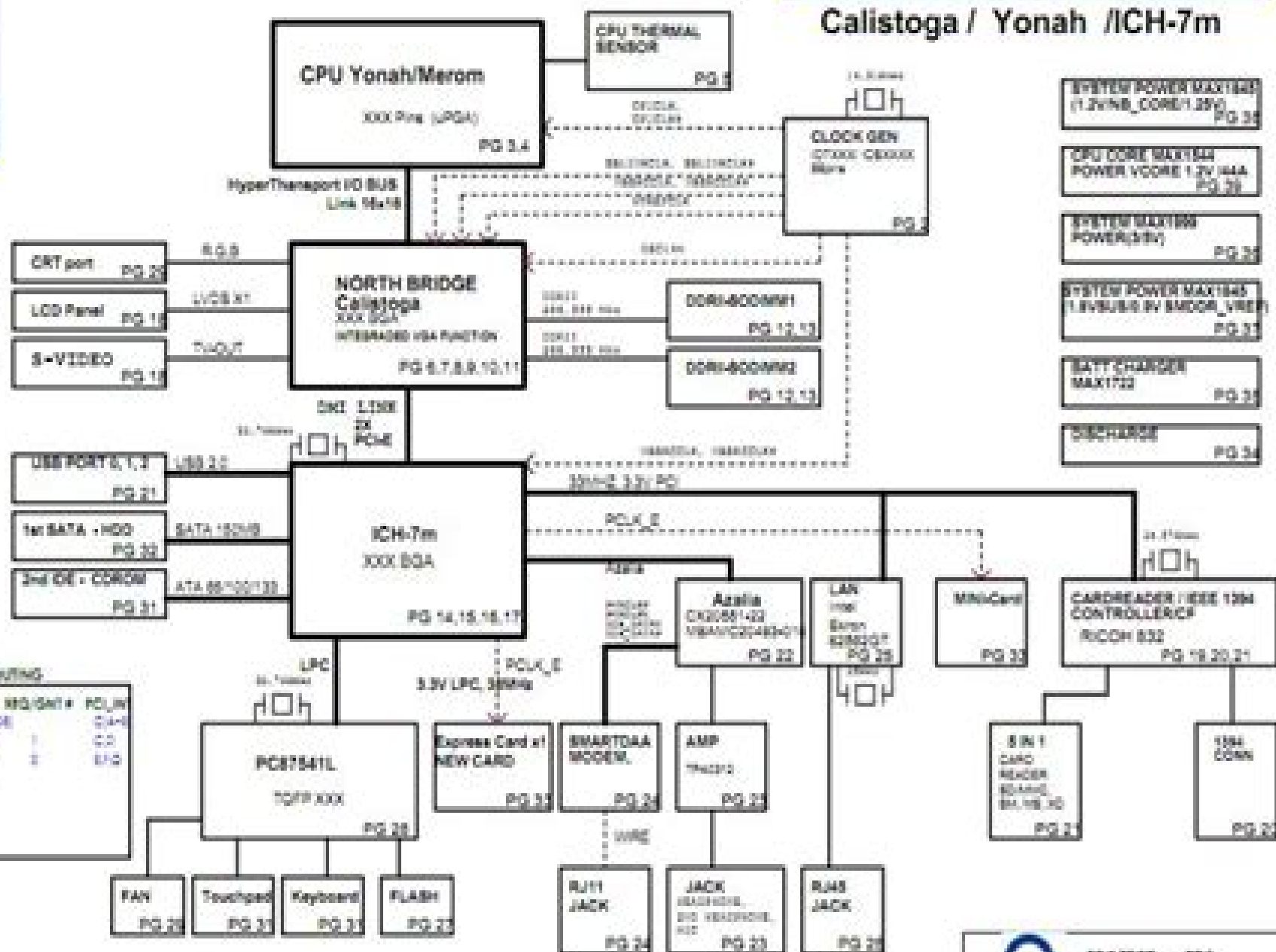
DIGITAL GND PG 34
Analog Ground PG 34
Power Board PG 34

PCI DEVICES I/O ROUTING

DEVICE	IOSEL #	REQ/DATA #	PCI_PIN
PCI Express	AG4-08		04-08
MiniPCI	AG0	1	02
Cardbus/PCMCIA	AG08	2	01-02

CT6 BLOCK DIAGRAM

Calistoga / Yonah /ICH-7m



Notebook Hp Pavilion Dv1000 Diagram

RD Boyd



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PC World ,2005 *PC Mag* ,2004-11-30 PCMag com is a leading authority on technology delivering Labs based independent reviews of the latest products and services Our expert industry analysis and practical solutions help you make better buying decisions and get more from technology *PC Magazine* ,2006 Focus ,2005 **HP Pavilion Laptop #3**
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