Cumulative Review

Chapter 1

Multiple Choice

For Exercises 1-10, choose the correct letter.

- Which property is illustrated by (3 + 5) + 7 = 3 + (5 + 7)?
 - A. Additive Identity

- C. Commutative Property of Addition
- B. Associative Property of Addition
- D. Distributive Property
- 2. Which algebraic expression represents the statement "4 more than the product of 6 and a number"? 1
 - 6.4a + 6
 - 6.6 4n

- $H_{*} = 4 6n$ 1. 6m + 4
- 3. What is the value of $-9 + (2^3 3^2)$?
 - A. -26

- -8

 $D_{\rm c} = 4$

- What is the value of √49? 6
 - E 1/7

G. 7

H. 14

- 1. 439
- **5.** What is the order of the numbers $\sqrt{12}$, -3.5, $\frac{5}{3}$, $-\frac{2}{3}$ from least to greatest? D
 - A. √T2, -3.5, 5, -2
 - B. √TZ, ⁵/₂, −²/₃, −3.5

- c. -3.5, $-\frac{2}{3}$, $\sqrt{12}$, $\frac{5}{3}$ D. -3.5, $-\frac{2}{3}$, $\frac{5}{3}$, $\sqrt{12}$
- Which ordered pair is not a solution of y = 2x + 1? H
 - F. (3, 7)

G. (0, 1)

H.(-1,1)

1.(-3, -5)

- Which expression is equivalent to −3.2(2x − 2.1)? A
 - $A_{x} = 6.4x + 6.72$

C. 6.4x + 6.72

 $B_{x} = 6.4x - 6.72$

- $D_{x} = 6.4x + 2.1$
- 8. Toby purchased 5 tickets online for a show. The tickets cost \$12 each plus there was a \$3.50 service fee for the order. How much money did Toby spend for the tickets? 1
 - F. \$15.50

G. \$51.50

H. \$60

L \$63.50

- What is the value of 3³ (4² 2³)?
 - $A_{r} = 1$

C. 19

D. 35

- Which expression is equivalent to 4(2x + 1) − (−6x)?
 - 6.14x + 4 6.8x 2
- $H_1 = 2x + 4$

L = 14x = 4

Prentice Hall Algebra 1 Chapter Answers

Ying Liu

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machine design methods are given equal weight and new ideas and design approaches are explored The productivity tools provided on the accompanying CD are outlined below 1 EXL Sim2002 logic simulator EXL Sim2002 is a full featured interactive schematic capture and simulation program that is ideally suited for use with the text at either the entry or advanced level of logic design Its many features include drag and drop capability rubber banding mixed logic and positive logic simulations macro generation individual and global or randomized delay assignments connection features that eliminate the need for wire connections schematic page sizing and zooming waveform zooming and scrolling a variety of printout capabilities and a host of other useful features 2 BOOZER logic minimizer BOOZER is a software minimization tool that is recommended for use with the text It accepts entered variable EV or canonical 1 s and 0 s data from K maps or truth tables with or without don't cares and returns an optimal or near optimal single or multi output solution. It can handle up to 12 functions Boolean functions and as many inputs when used on modern computers 3 ESPRESSO II logic minimizer ESPRESSO II is another software minimization tool widely used in schools and industry It supports advanced heuristic algorithms for minimization of two level multi output Boolean functions but does not accept entered variables It is also readily available from the University of California Berkeley 1986 VLSI Tools Distribution 4 ADAM design software ADAM for Automated Design of Asynchronous Machines is a very powerful productivity tool that permits the automated design of very complex asynchronous state machines all free of timing defects The input files are state tables for the desired state machines The output files are given in the Berkeley format appropriate for directly programming PLAs ADAM also allows the designer to design synchronous state machines timing defect free The options include the lumped path delay LPD model or NESTED CELL model for asynchronous FSM designs and the use of D FLIP FLOPs for synchronous FSM designs The background for the use of ADAM is covered in Chapters 11 14 and 16 of the REVISED 2nd Edition 5 A OPS design software A OPS for Asynchronous One hot Programmable Sequencers is another very powerful productivity tool that permits the design of asynchronous and synchronous state machines by using a programmable sequencer kernel This software generates a PLA or PAL output file in Berkeley format or the VHDL code for the automated timing defect free designs of the following a Any 1 Hot programmable sequencer up to 10 states b The 1 Hot design of multiple asynchronous or synchronous state machines driven by either PLDs or RAM The input file is that of a state table for the desired state machine This software can be used to design systems with the capability of instantly switching between several radically different controllers on a time shared basis The background for the use of A OPS is covered in Chapters 13 14 and 16 of the REVISED 2nd Edition Cataloa of Copyright Entries. Third Series Library of Congress. Copyright Office, 1968 Mathematical Fallacies, Flaws, and Flimflam Edward J. Barbeau, 2000-12-31 A collection of mathematical errors drawn from the work of students textbooks and the media as well as from professional mathematicians themselves Yakov G. Berkovich; Lev S. Kazarin; Emmanuel M. Zhmud': Characters of Finite Groups. Volume 2 Yakov G. Berkovich, Lev S. Kazarin, Emmanuel M. Zhmud', 2018-12-17 This updated

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