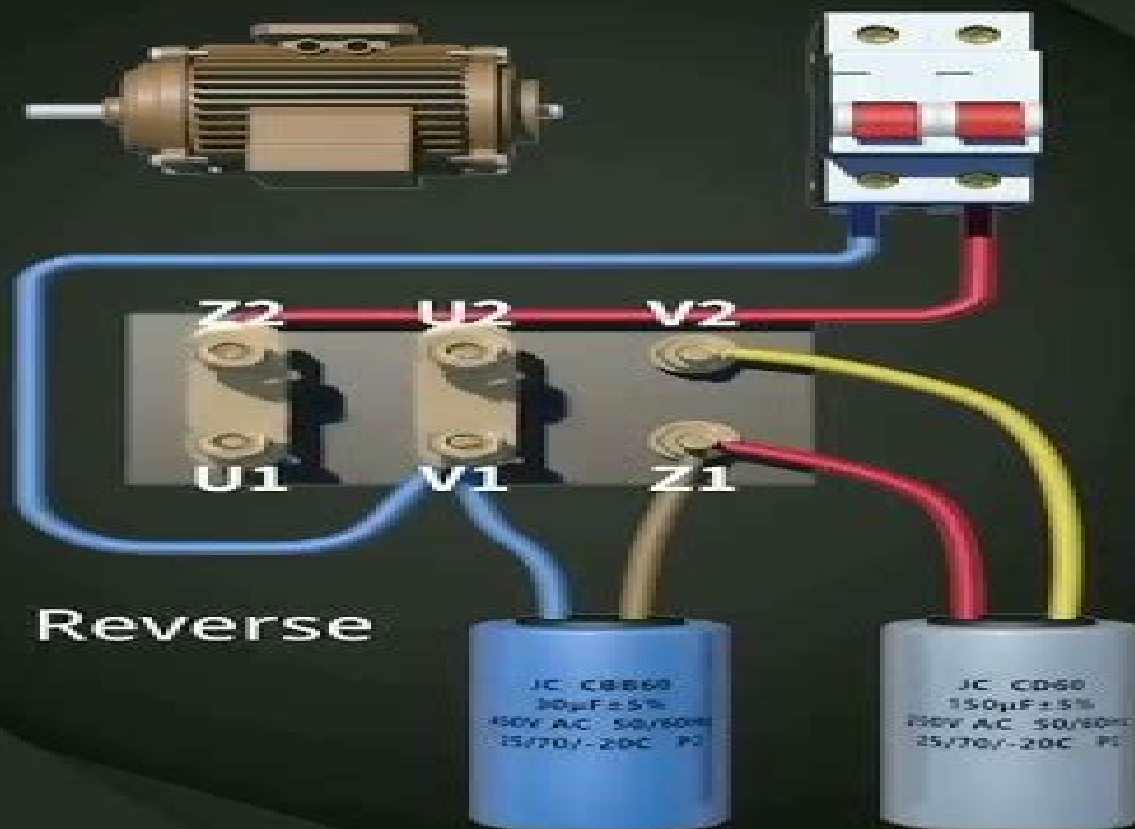


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Nanosecond Electron Accelerators Sergey Sokolov, 2025-06-04 The development of the technique of generating high voltage nanosecond pulses based on the effect of a nanosecond opening switch in semiconductor diodes gave the prerequisites for the development of nanosecond electron accelerators with a pulse periodic operation mode Such accelerators generate high intensity large beams that have a strong biological effect However to create industrial accelerators and implement highly efficient radiation technologies it is necessary to provide the required range of beam parameters high reliability and long life of accelerators This book describes the principles and design of repetitive nanosecond electron accelerators as well as the development technologies and specific radiation technologies based on them It discusses the application of the created accelerators in various branches of science and technology in particular for radiation sterilization and disinfection of medical and food products correction of defects in solids and radiation chemical technologies The book also covers the issues encountered in measuring the parameters of electron beams monitoring parameters and automating the control of accelerators

Basic Theory and Application of Transistors United States. Department of the Army, 1959 Technical Manual United States. War Department, 1951 Control of Machines Mr. Ajeet Singh, 2018-05-13 EduGorilla has its own publishing wing producing exam prep books trade books etc *Electronic Circuit Analysis*: Rao, 2011 Electronic Circuit Analysis is designed to serve students of a two semester undergraduate course on electronic circuit analysis It builds on the subject from its basic principles over fifteen chapters providing detailed coverage on the design and analysis of electronic circuits

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electronic circuits **The Electrical Engineering Handbook, Second Edition** Richard C. Dorf, 1997-09-26 In 1993 the first edition of The Electrical Engineering Handbook set a new standard for breadth and depth of coverage in an engineering reference work Now this classic has been substantially revised and updated to include the latest information on all the important topics in electrical engineering today Every electrical engineer should have an opportunity to expand his expertise with this definitive guide In a single volume this handbook provides a complete reference to answer the questions encountered by practicing engineers in industry government or academia This well organized book is divided into 12 major sections that encompass the entire field of electrical engineering including circuits signal processing electronics electromagnetics electrical effects and devices and energy and the emerging trends in the fields of communications digital devices computer engineering systems and biomedical engineering A compendium of physical chemical material and mathematical data completes this comprehensive resource Every major topic is thoroughly covered and every important concept is defined described and illustrated Conceptually challenging but carefully explained articles are equally valuable to the practicing engineer researchers and students A distinguished advisory board and contributors including many of the leading authors professors and researchers in the field today assist noted author and professor Richard Dorf in offering complete coverage of this rapidly expanding field No other single volume available today offers this combination of broad coverage and depth of exploration of the topics The Electrical Engineering Handbook will be an invaluable resource for electrical engineers for years to come **Electronic Devices and Circuits** Cheruku Dharma Raj, Cheruku, 2008 *Design of High-speed Communication Circuits* Ramesh Harjani, 2006 MOS technology has rapidly become the de facto standard for mixed signal integrated circuit design due to the high levels of integration possible as device geometries shrink to nanometer scales The reduction in feature size means that the number of transistor and clock speeds have increased significantly In fact current day microprocessors contain hundreds of millions of transistors operating at multiple gigahertz Furthermore this reduction in feature size also has a significant impact on mixed signal circuits Due to the higher levels of integration the majority of ASICs possesses some analog components It has now become nearly mandatory to integrate both analog and digital circuits on the same substrate due to cost and power constraints This book presents some of the newer problems and opportunities offered by the small device geometries and the high levels of integration that is now possible The aim of this book is to summarize some of the most critical aspects of high speed analog RF communications circuits Attention is focused on the impact of scaling substrate noise data converters RF and wireless communication circuits and wireline communication circuits including high speed I O Contents Achieving Analog Accuracy in Nanometer CMOS M P Flynn et al Self Induced Noise in Integrated Circuits R Gharpurey High Speed Oversampling Analog to Digital Converters A Gharbiya et al Designing LC VCOs Using Capacitive Degeneration Techniques B Jung Fully Integrated Frequency Synthesizers A Tutorial S T Moon et al Recent Advances and Design Trends in CMOS Radio Frequency Integrated Circuits D J Allstot et al Equalizers for High

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