

## CBCS Scheme

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15CS32

Third Semester B.E. Degree Examination, Dec.2017/Jan.2018  
Analog and Digital Electronics (ADE)

Time: 3 hrs.

Max. Marks: 80

Note: Answer any FIVE full questions, choosing one full question from each module.

Module-1

1. a. Explain the operation and characteristics of N-channel JFET. (08 Marks)  
 b. With block diagram, explain the operation of a Astable multivibrator using IC 555. (08 Marks)

OR

2. a. With circuit diagram, explain the operation of a Relaxation oscillator. (06 Marks)  
 b. Fig. Q2(b), shows a Biasing configuration using DEMOSFET gives that the saturation drain current is 8mA and the pinch off voltage is -2V.



Fig. Q2(b)

- c. Determine the value of gate source voltage drain current of drain source voltage. (06 Marks)  
 d. Write the advantages of MOSFET over JFET. (04 Marks)

Module-2

3. a. Give the simplest logic circuit for following logic equation where d represents don't care condition for following equations:  
 $E(A, B, C, D) = \sum m(7) + d(10, 11, 12, 13, 14, 15)$  (06 Marks)  
 b. Simplify the following Boolean function by using Quine – McClusky method.  
 $E(A, B, C, D) = \sum m(0, 2, 3, 6, 7, 8, 10, 12, 13)$  (10 Marks)

OR

4. a. What are Hazards? Explain the types of Hazards and it covers. (08 Marks)  
 b. Discuss Briefly an HDL Implementation models. (04 Marks)  
 c. Explain the concept of Duality in Digital circuits. (04 Marks)

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**Anurag Mishra,Deepak Gupta,Girija  
Chetty**



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