

Bachelor of Engineering in Electrical Engineering – Stage 1
(NFQ Level 7)

Summer 2007

Industrial Electronics

(Time: 3 Hours)

INSTRUCTIONS:

Answer Five Questions.

All formula and calculations must be shown.

Examiners:

Mr. R. Daly

Prof. E. McQuade

Mr. M. Hennessy

- Q1 (a) What is the purpose of rectifiers in electronic equipment?

2 marks

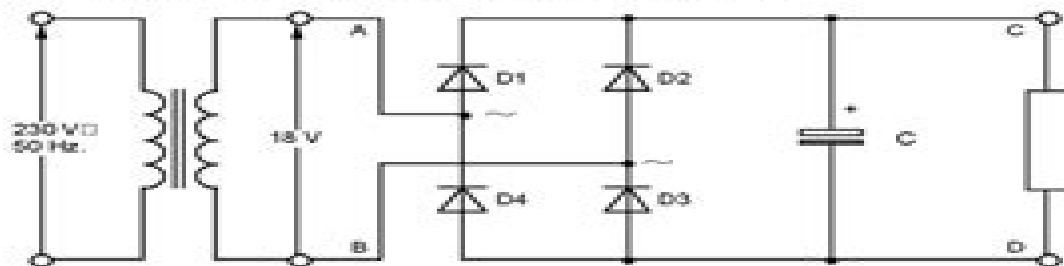


Fig. 1

- (b) Using the reference points in Fig. 1, explain (ignoring the capacitor) the operation of the full-wave bridge rectifier circuit and indicate the polarity at the output terminals C and D. 5 marks
- (c) Calculate for the rectifier:
- (i) The maximum voltage (V_m) across the rectifier supply terminals AB, 3 marks
 - (ii) The peak voltage (V_p) across the load terminals CD, 3 marks
 - (iii) The average voltage $V_{d.c.}$ across the load if V_{kar} is 20% of V_p . 4 marks
- Q2 (a) Construct a 5-column binary table with appropriate headings to represent the decimal numbers from 0 to 15. Write the decimal digits in the left hand column. 6 marks
- (b) Draw the circuit logic symbols and truth tables for, a 2-input AND gate, and a 2-input XOR gate. 6 marks
- (c) Show how the gates in (b) can be connected to construct a half-adder. Include in your answer the truth table for the half-adder. 8 marks

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Table of Contents Nindustrial Electronics Past Exam Papers

1. Understanding the eBook Nindustrial Electronics Past Exam Papers
 - The Rise of Digital Reading Nindustrial Electronics Past Exam Papers
 - Advantages of eBooks Over Traditional Books
2. Identifying Nindustrial Electronics Past Exam Papers
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Nindustrial Electronics Past Exam Papers
 - User-Friendly Interface
4. Exploring eBook Recommendations from Nindustrial Electronics Past Exam Papers
 - Personalized Recommendations
 - Nindustrial Electronics Past Exam Papers User Reviews and Ratings
 - Nindustrial Electronics Past Exam Papers and Bestseller Lists
5. Accessing Nindustrial Electronics Past Exam Papers Free and Paid eBooks
 - Nindustrial Electronics Past Exam Papers Public Domain eBooks
 - Nindustrial Electronics Past Exam Papers eBook Subscription Services

- Nindustrial Electronics Past Exam Papers Budget-Friendly Options
- 6. Navigating Nindustrial Electronics Past Exam Papers eBook Formats
 - ePub, PDF, MOBI, and More
 - Nindustrial Electronics Past Exam Papers Compatibility with Devices
 - Nindustrial Electronics Past Exam Papers Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Nindustrial Electronics Past Exam Papers
 - Highlighting and Note-Taking Nindustrial Electronics Past Exam Papers
 - Interactive Elements Nindustrial Electronics Past Exam Papers
- 8. Staying Engaged with Nindustrial Electronics Past Exam Papers
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Nindustrial Electronics Past Exam Papers
- 9. Balancing eBooks and Physical Books Nindustrial Electronics Past Exam Papers
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Nindustrial Electronics Past Exam Papers
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Nindustrial Electronics Past Exam Papers
 - Setting Reading Goals Nindustrial Electronics Past Exam Papers
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Nindustrial Electronics Past Exam Papers
 - Fact-Checking eBook Content of Nindustrial Electronics Past Exam Papers
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends

- Integration of Multimedia Elements
- Interactive and Gamified eBooks

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