## Pre-Lab 1: Half-Wave Rectifier

### INTRODUCTION

Diodes are a critical part of DC power supplies. The half-wave rectifier is the simplest type of AC-to-DC converter. The overall goal of PreLab1 is to simulate some half-wave rectifier circuits that you will test during the lab session.

#### GENERAL GUIDELINES

- 1) Each student must turn in his/her own Pre-Lab assignment (include both schematics and waveforms).
- Students are allowed (even encouraged) to work together. However, you must turn in your own work! Turning in multiple printouts of the same schematic/waveforms (with different student names) will result in ZERO points.
- You will be using the program Multisim for all simulations. The program is installed on all of the computers in N100, N102, and N106.

NOTE: There will be two lab exams during the term. Each lab exam will test each student's ability to use Multisim and build/test a circuit.

### PART 1: HALF-WAVE RECTIFIER

TASK 1a: Simulate the half-wave rectifier shown below. Use the tutorial on the WEB site to get started.

- The voltage source is a sine wave generator (10V<sub>FF</sub>, 60 Hz) in series with a 50 ohm resistor. This is a pretty good approximation of the Agilent 33220 function generators in N100.
- Note that 10 V<sub>PP</sub> means peak-to-peak, so the sine wave amplitude is actually 5 V.
- The 1N4002 diode can be selected from the place menu by selecting: component | Diodes | Diode | 1N4004G.
- The resistor is 10 kohm.
- From the menu bar select: simulate | Analysis | Transient. Set the End Time to .05 s and the minimum number of points to 1000.
- After running the simulation a "Grapher" screen will pop up. Turn on the Grid. From the Cursor menu select show cursers and then select to the next Y MAX =>. After the cursor goes to the maximum point select Add Data Label at Cursor.
- Your simulated waveform should look like the graph shown below.
- Turn in both the schematic and waveform.

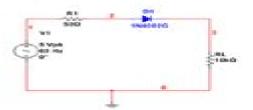


Figure 1: Circuit schematic and simulated load voltage.

# **Rectifier Lab Manual**

**Clemens Wendtner** 



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