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Lakeland Boating ,1994 Personal Watercraft (PWC) Identification ,2000 Consumers Index to Product Evaluations and Information Sources Pierian Press, 1995-03 **Consumers Index to Product Evaluations and Information Sources** ,1999 SELOC's Bombardier Sea-doo Personal Watercraft, Volume IIA, Daily Graphs ,1988 1992-1997: Tune-up and Repair Manual Seloc, 1998 SELOC Marine maintenance and repair manuals offer the most comprehensive authoritative information available for outboard inboard stern drive and diesel engines as well as personal watercraft SELOC has been the leading source of how to information for the marine industry since 1974 Designed and written to serve the needs of the professional mechanic do it yourself boat enthusiast instructor and student these manuals are based on actual teardowns done by Chilton Marine's editors authors in our on site facility Providing complete coverage on everything from basic maintenance to engine overhaul every manual features Simple to follow step by step illustrated procedures Hundreds of exploded drawings photographs and tables Troubleshooting sections accurate specifications and wiring diagrams Recognized and used by technical trade schools as well as the U S military Covers all GS GSI GSX GTI GTS HX SP SPI SPX and XP models SPi User Manual V0.1, 2011 This document describes the Serial Powering Interface SPi ASIC SPi is a general purpose ASIC prototype designed for use in serial powering of silicon detector instrumentation This description is written as a user manual to aid application not as a design description SPi is a generic custom ASIC manufactured in 0 25 mu m CMOS by TSMC to interface between a constant current source and silicon detector read out chips There is no SEU single event upset protection but most not all components are radiation tolerant design An operating voltage of 1 2 to 2 5 volts and other design features make the IC suitable for a variety of serial powering architectures and ROICs It should be noted that the device is likely to be a prototype for demonstration rather than a product for inclusion in a detector The next design s SPin are likely to be designed for a specific application eg SLHC The component includes 1 Seven bi directional LVDS like buffers for high data rate links to from the read out chips These are AC coupled series capacitor off chip for DC level conversion 2 A programmable internal programmable shunt regulator to provide a defined voltage to readout chips when linked in a serial powering chain 3 A programmable internal shunt regulator control circuit for external transistor control 4 Shunt current measurement for internal shunt regulator 5 A programmable internal shunt regulator current alarm and 6 Two programmable linear regulators Guide Spi Anne-Caroline Valade, Sophie Meunier, François-Gonzague Meunier, 2011-05-01 Le Guide Spi est un support adapt tous petits et grands Il permet de prendre un temps de pri re tout instant de la journ e et de la semaine l int rieur une v ritable bo te outils pour la pri re L essentiel de ce que tout Chr tien doit conna tre et savoir utiliser Un v ritable livre objet Son format original permet un transport pratique et discret Les mod les Tendances toucheront toutes les sensibilit s La possibilit de choisir entre tous les mod les conduira les acheteurs ne pas trancher et prendre plusieurs mod les Son prix attractif finira de convaincre Le choix

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