

Computer Science - Computer Science Test - Computer Organisation & Architecture - COA Online Test 1

No. Question

1. A low memory can be connected to microprocessor by using
 - A INTER
 - B RESET IN
 - C Both (a) and (b)
 - D READY
2. A basic instruction that can be interpreted by a computer generally has
 - A An operand and an address
 - B A decoder and an accumulator
 - C Sequence register and decoder
 - D None of these
3. The addressing mode used in an instruction of the form ADD X, Y, is
 - A absolute
 - B immediate
 - C indirect
 - D Relative
4. Number of machine cycles required for RET instruction in 8085 microprocessor is
 - A 1
 - B 2
 - C 3
 - D None of these
5. The 8085 microprocessor responds to the presence of an interrupt
 - A As soon as the TRAP pin becomes 'LOW'
 - B By checking the TRAP pin for 'high' status at the end of each instruction fetch
 - C By checking the TRAP pin for 'high' status at the end of the execution of each instruction
 - D By Checking the TRAP pin for 'high' status at regular intervals
6. The TRAP interrupt mechanism of the 8085 microprocessor executes
 - A An RST by hardware
 - B The instruction supplied by external device through the INTA signal
 - C An instruction from memory location 20 H
 - D None of these
7. The address range to which I/O chip will respond is
 - A 0000 H to FFFF H
 - B 0000 H to 5FFF H
 - C 4000 H to 5FFF H
 - D None of these
8. What is the average access time for a drum rotating at 4000 revolutions per minute?
 - A 2.5 mill seconds
 - B 5.0 mill seconds
 - C 7.5 mill seconds
 - D 10.0 mill seconds
9. What is the byte capacity of a drum which is 5 inch high, 10 inch diameter, and which has 60 tracks per inch bit density of 800 bits per inch ?
 - A 94200 bytes
 - B 471000 bytes
 - C 18840 bytes
 - D 16384 bytes
10. The memory address range to which RAM will respond
 - A 0000 H to 1 FFF H
 - B 0000 H to 5FFF H
 - C 4000 H to 5FFF H
 - D 3000 H to FFFF H

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J Rink



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