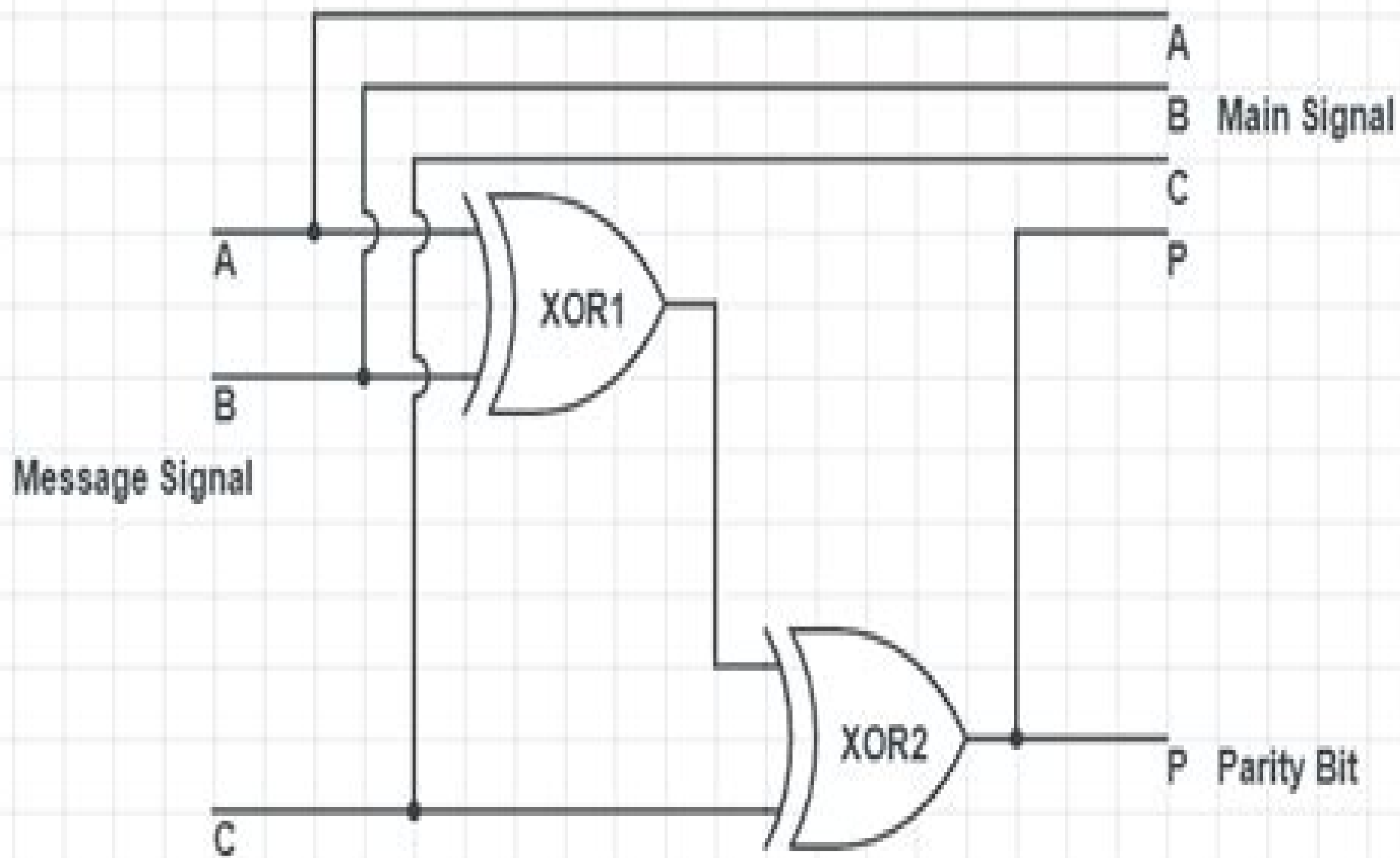


3-bit Even Parity Generator



Parity Bit Generator Circuit Diagram

Christian G. Meyer

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