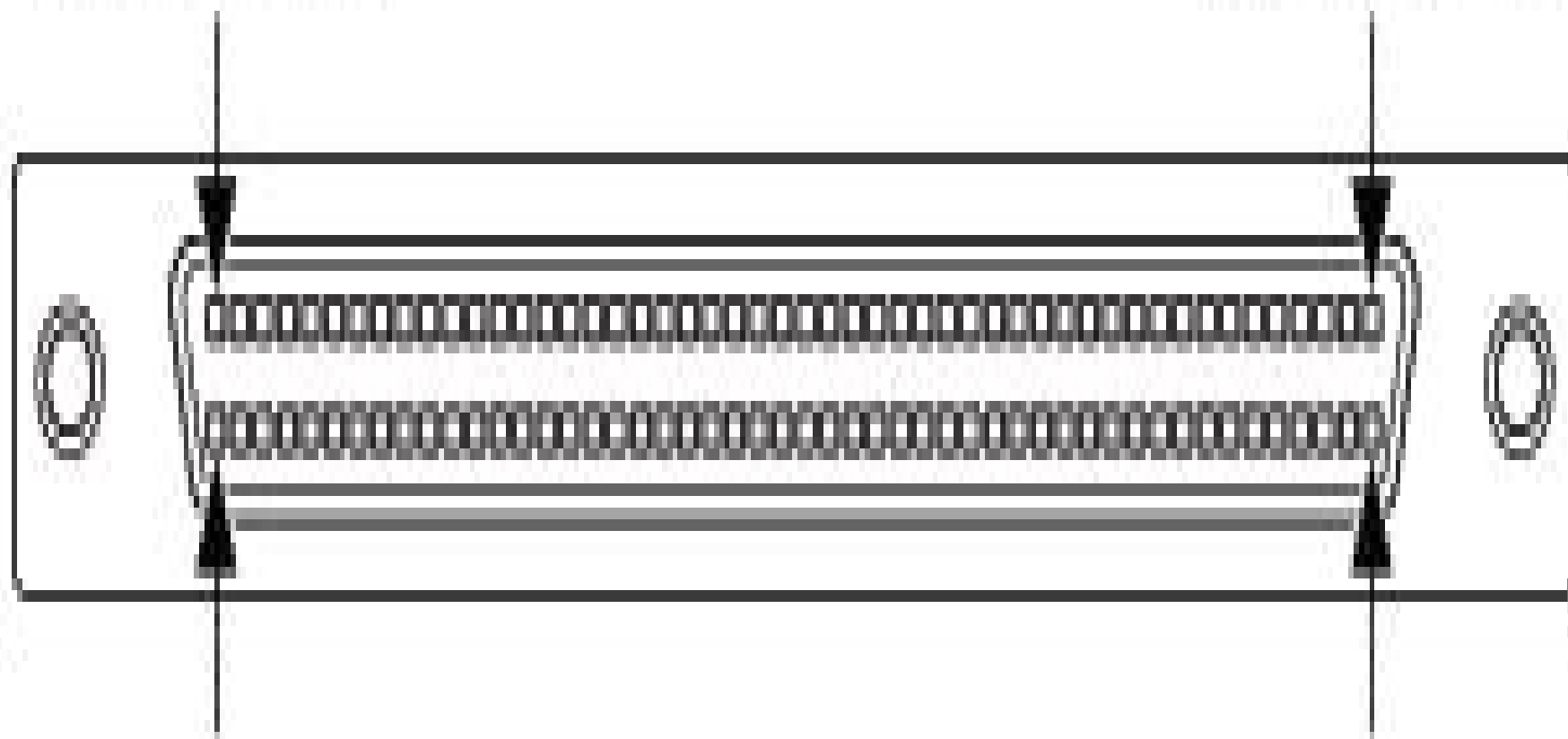


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Sabine Zange

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Microprocessors & Microcontrollers Atul P. Godse, Dr. Deepali A. Godse, 2021-01-01 The book is written for an undergraduate course on the 8086 microprocessor and 8051 microcontroller It provides comprehensive coverage of the hardware and software aspects of 8086 microprocessor and 8051 microcontroller The book is divided into three parts The first part focuses on 8086 microprocessor It teaches you the 8086 architecture instruction set Assembly Language Programming ALP interfacing 8086 with support chips memory and peripherals such as 8251 8253 8255 8259 8237 and 8279 It also explains the interfacing of 8086 with data converters ADC and DAC and introduces a traffic light control system The second part focuses on multiprogramming and multiprocessor configurations numeric processor 8087 I O processor 8089 and introduces features of advanced processors such as 80286 80386 80486 and Pentium processors The third part focuses on 8051 microcontroller It teaches you the 8051 architecture instruction set programming 8051 and interfacing 8051 with external memory It explains timers counters serial port interrupts of 8051 and their programming It also describes the interfacing 8051 with data converters ADC and DAC keyboards LCDs LEDs stepper motors and sensors

The Designer's Guide to the Cortex-M Processor Family Trevor Martin, 2022-12-02 The Designer's Guide to the Cortex M Microcontrollers Third Edition provides an easy to understand introduction to the concepts required to develop programs in C with a Cortex M based microcontroller Sections cover architectural descriptions that are supported with practical examples enabling readers to easily develop basic C programs to run on the Cortex M0 M0 M3 and M4 and M7 and examine advanced features of the Cortex architecture such as memory protection operating modes and dual stack operation Final sections examine techniques for software testing and code reuse specific to Cortex M microcontrollers Users will learn the key differences between the Cortex M0 M0 M3 and M4 and M7 how to write C programs to run on Cortex M based

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