

II PU ELECTRONICS (40) MODEL

QUESTION PAPER

Time: 3 Hour 15 min

Max. Marks: 70

Instructions:

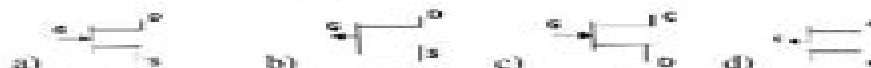
1. The question paper has four parts A, B, C and D.
2. Part - A is compulsory.
3. Part -D (Section 1) consists of essay type questions and Section-2 Problems.
4. Circuit diagrams, timing diagrams and truth tables must be drawn wherever necessary.
5. Solve the problems with necessary formulas.

PART A

I. Select the correct answer from the choices given:

15 x 1 = 15

1. The correct circuit symbol for N-channel JFET is



2. In which transistor region the collector current is almost constant in CE output characteristics

a) Cutoff Region b) active region c) Saturation region d) Ohmic region

3. Which of the following transistor Amplifier has highest voltage gain

a) CB Amplifier b) CC Amplifier c) CE Amplifier d) CS Amplifier

4. Which among the below statements are true w.r.t. voltage series type of negative feedback

Statement I: It increases the bandwidth

Statement II: Decreases the output impedance

- a) I is true and II is false b) I is false and II is true
c) I and II both are true d) I and II both are false

5. The OP AMP amplifier circuit with feedback resistance $2\text{ K}\Omega$ and another resistance of $1\text{ K}\Omega$ produces the output voltage of -2.2 V when the input is applied to its pin 2. The input voltage to the circuit must be

a) 2.2 V b) -1 V c) 1 V d) -2.2 V

6. The OPAMP circuit which acts as a high-pass filter

a) Differentiator b) Integrator c) Adder d) Subtractor

7. Which of the following is a RC Oscillator

a) Phase shift oscillator b) Hartley oscillator
c) Colpitts's oscillator d) Crystal oscillator

8. The ionosphere layers persist during both day and night time among the following list are

1) D layer 2) F layer 3) E layer

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