GBGS Scheme

URSEW.

1.5 Ca5050

Third Semester B.E. Degree Examination, Dec.2017/Jan.2018 Analog and Digital Electronics (ADE)

Times: 3 hrs.

Note: Answer any FIVE full questions, choosing one full question from each module

Millordin he-II.

- Explain the operation and characteristics of N-channel JFET.
 - With block diagram, explain the operation of a Astable multivibrate using IQ

grow 244 methods good believely a

- With circuit diagram, explain the operation of a Relaxation oscillate 696 Minerkali
 - Fig. Q2(b), shows a Biasing configuration using DEMOSPET given that the saturation drain current is 8mA and the pinch off voltage is -2V.



ist. Q2(6)

O You was a

Determine the value of gate source witage drain current of drain source voltage. (06 Marks) Write the advantages of MOSFET over JFET. CONTRACTOR OF

Module-2

- a. Give the simplest logic circuit for following logic equation where d represents don't care condition four flowing logations:
 (8. 8. 9. 2.17.7) + d (10, 11, 12, 13, 14, 15). (06 Marks)
 b. Simplify the following Boolean function by using Quine McClusky method.

 - $\mathbf{S}(\mathbf{A}, \mathbf{B}(\mathbf{G}, \mathbf{D}) = \sum_{i} m(0, 2, 3, 6, 7, 8, 10, 12, 13).$ (DO Minimize)

OHR:

- What are Hazards? Explain the types of Hazards and it covers.
 - Discuss Briefly an HDL Implementation models. Explains the concept of Duality in Digital circuits.

- (04 Marks)
- (04 Marries)

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