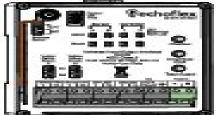
Overview

The Universal input-Output interface (UIO) facilitates the creation of hybrid solutions by integrating Echoflex wireless devices into existing panels, wired control systems, or building energy management systems. It provides functionality to support hospitality applications and timeclocks, connect shared occupancy spaces, and recognize presets.



The UIO's two hardware input channels support
0-10 V or 4-20 mA signals and have wireless
outputs assigned to translate analog or digital signals into a number of
wireless message types. In addition to gateways or controllers, another
UIO can be linked to reproduce a sensor's analog or digital value.

Two output channels deliver 0–10 V or 4–20 mA signals driven by wireless devices linked to the channel. Combining the channels can replicate an Echoflex ELED1 controller with dimming (CH1) and relay control (CH2). Each output channel interprets a wireless message and outputs an analog value on the selected channel.

This document covers installation, testing, and settings that apply to all UIO models. Garibaldi Pro software, Echoflex's in-field commissioning tool, is required to configure additional settings. The software is available for download at echoflexsolutions.com.

The product package includes the interface and two 3-pole WAGO connectors.

Prepare for Installation

To ensure optimal function, consider the installation environment and the following guidelines:

- Avoid spaces with high density construction materials and large metal appliances or fixtures as they may disrupt wireless reception.
- For optimal performance, switches and sensors should be installed within a 24 m (80 ft) uno betructed view of the interface. Consider adding a repeater to extend the wireless signal, if required.
- Mount the interface to an electrical junction box (preferably non-metallic) or a panel in a location and at a height where it is not subject to tampering by unauthorized personnel.

Optional Input Output Board Installation Guide

Brooks

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Technical Manual for Univac Digital Trainer United States. Bureau of Naval Personnel, 1965 The AT&T **Documentation Guide** ,1993-06 Catalog of the most often requested AT T documents **PC Hardware Configuration** Guide for DOS and Solaris Ron Ledesma, 1994 Written in non technical language supplemented with how to illustrations checklists and rules of thumb this configuration and planning guide aims to take the mystery out of the technical configuration details and walk readers step by step through a simple structured approach for making the critical decisions that will ultimately result in trouble free computer and network hardware operation It shows how to determine hardware requirements select compatible hardware configure hardware components design the physical network and troubleshoot Ocean Systems Technician 3 & 2 (maintainer). Roy M. Derrick, 1985 installation problems 20-sim 4C 2.0 Reference Manual, A+ Certification Hardware Stand Alone Text Brooks, 2004 Packaged with the exciting examGEAR TM CD ROM this superbly written and illustrated book lab manual is the perfect preparatory tool for developing the knowledge ability and skills currently demanded by the computer industry Most computer technician employers now require the industry standard A Certification and this valuable system includes dynamic interactive online updates for each chapter It also provides for any changes encountered in the A exams over the life of this exam version making your investment in this book never out of date or incomplete Topics include basic PC hardware advanced system boards standard I O systems mass storage systems data communications printers portable systems preventive maintenance and basic system troubleshooting For computer technicians and others preparing to take the industry standard A certification exam InfoWorld ,1988-12-05 InfoWorld is targeted to Senior IT professionals Content is segmented into Channels and Topic Centers InfoWorld also celebrates people companies and projects ASSEMBLY LANGUAGE PROGRAMMING IN GNU/LINUS FOR IA32 ARCHITECTURES RAJAT MOONA, 2009-01-14 This book provides an easy to understand step by step approach to learning the fundamentals of Assembly language programming for Intel's architectures using a GNU Linux based computer as a tool Offering students of computer science and engineering a hands on learning experience the book shows what actions the machine instructions perform and then presents sample programs to demonstrate their application The book is suitable for use during courses on Microprocessors Assembly language programming and Computer Organization in order to understand the execution model of processors This knowledge also helps strengthen concepts when students go on to study operating systems and compiler construction The concepts introduced are reinforced with numerous examples and review exercises An Instructor's CD provides all the programs given in the book and the solutions to exercises Key Features Discusses programming guidelines and techniques of using Assembly language programs Shows techniques to interface C and Assembly language programs Covers instructions from general purpose instruction sets of IA32 processors Includes MMX and MMX 2 instructions Covers SSE and SSE 2 instructions Explains input output techniques and their use in GNU

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