

(OR) b) Mention the various energy losses in a transformer.

Energy losses in a transformer :

(i) Core loss or Iron loss :

- ▲ Hysteresis loss and eddy current loss are known as core loss or Iron loss.
- ▲ When transformer core is magnetized or demagnetized repeatedly by the alternating voltage applied across primary coil, hysteresis takes place and some energy is lost in the form of heat. It is minimized by using **silicone steel** in making transformer core.
- ▲ Alternating magnetic flux in the core induces eddy currents in it. Therefore there is energy loss due to the flow of eddy current called eddy current loss. It is minimized by using **very thin laminations** of transformer core.

(ii) Copper loss :

- ▲ The primary and secondary coils in transformer have electrical resistance.
- ▲ When an electric current flows through them, some amount of energy is dissipated due to Joule's heating and it is known as copper loss.

It is minimized by using **wires of larger diameter (thick wire)**

(iii) Flux leakage :

- ▲ The magnetic flux linked with primary coil is not completely linked with secondary. Energy loss due to this flux leakage is minimized by **winding coils one over the other**.

36.a) Derive an expression for phase angle between the applied voltage and current in a series RLC circuit.

Series RLC circuit :

- ▲ Consider a circuit containing a resistor of resistance 'R', an inductor of inductance 'L' and a capacitor of capacitance 'C' connected across an



$$v = V_m \sin \omega t$$

- ▲ The applied alternating voltage is given by,

$$v = V_m \sin \omega t \quad \text{--- (1)}$$
- ▲ Let 'i' be the current in the circuit at that instant.
- ▲ Hence the voltage developed across R, L and C

$$V_R = iR \quad (V_R \text{ is in phase with } i)$$

$$V_L = iX_L \quad (V_L \text{ leads } i \text{ by } \frac{\pi}{2})$$

$$V_C = iX_C \quad (V_C \text{ lags } i \text{ by } \frac{\pi}{2})$$
- ▲ The phasor diagram is drawn by representing current along OA' , V_R along OB' , V_L along OD' and V_C along OC'



- ▲ If $V_L > V_C$, then the net voltage drop across LC combination is $(V_L - V_C)$ which is represented by AD'
- ▲ By parallelogram law, the diagonal OE' gives the resultant voltage 'v'

$$v = \sqrt{V_R^2 + (V_L - V_C)^2}$$

$$v = \sqrt{(iR)^2 + (iX_L - iX_C)^2}$$

$$v = i\sqrt{R^2 + (X_L - X_C)^2} \quad \text{--- (4)}$$

$$\text{(or)} \quad i = \frac{v}{\sqrt{R^2 + (X_L - X_C)^2}} \quad \text{--- (4)}$$

$$\text{(or)} \quad i = \frac{v}{Z} \quad \text{--- (5)}$$

- ▲ Where, $Z = \sqrt{R^2 + (X_L - X_C)^2}$ is called impedance of the circuit, which refers to the effective opposition to the circuit current by the series RLC circuit.

- ▲ From the phasor diagram, the phase angle between 'v' and 'i' is found out by

$$\tan \phi = \frac{V_L - V_C}{V_R} = \frac{X_L - X_C}{R} \quad \text{--- (6)}$$

Special cases :

- When $X_L > X_C$, the phase angle ϕ is positive. It means that v leads i by ϕ .
 (I.e.) $v = V_m \sin \omega t$ & $i = I_m \sin(\omega t - \phi)$
 This circuit is inductive.
- When $X_L < X_C$, the phase angle ϕ is negative. It means that v lags behind i by ϕ .
 (I.e.) $v = V_m \sin \omega t$ & $i = I_m \sin(\omega t + \phi)$
 This circuit is capacitive.
- When $X_L = X_C$, the phase angle ϕ is zero. It means that v is in phase with i .
 (I.e.) $v = V_m \sin \omega t$ & $i = I_m \sin \omega t$

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