

Mark Scheme (Results)

Summer 2013

GCE Further Pure Mathematics 2 (6668/01R)

Pixl Club Maths Mark Scheme 2013 Edexcel

Christian G. Meyer



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sep 12 2022 figure 15 4 1 15 4 1 a an rlc series circuit b a comparison of the generator output voltage and the current the value of the phase difference ϕ depends on the values of r c and l since the elements are in series the same current flows through each element at all points in time the relative phase between the current and the emf

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voiceover we now begin a whole new area of circuit analysis called sinusoidal steady state analysis you can also call it ac analysis ac stands for alternating current it means it is a voltage or a current that where the signal actually changes sign it is

positive sometimes it is negative sometimes

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about transcript the ratio of a sinusoidal voltage to a sinusoidal current is called impedance this is a generalization of ohm's law for resistors we derive the impedance of a resistor inductor and capacitor the inductor and capacitor impedance includes a term for frequency so the impedance of these components depends on frequency

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find the voltage source equivalent of a current source and vice versa compute voltages and currents in multi source rlc networks using superposition simplify rlc networks using Thévenin's and Norton's theorems determine conditions for maximum power transfer and compute the maximum power

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ω represents the frequency of a sine wave when we write it this way $\sin \omega t$ if $\omega = 1$ the sin completes one cycle in 2π seconds if $\omega = 2\pi$ the sin completes one cycle sooner every 1 second for $f = 1$ if $\omega = 2\pi f$ the sin completes one cycle even faster every $1/f$ seconds or equivalently sin completes f cycles in 1 second

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