

# HW: Logic Gate Worksheet

1. A Logic circuit has two inputs being X and Y fill in all the possible combinations.

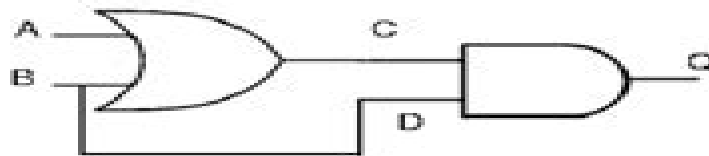
| X | Y |
|---|---|
|   |   |
|   |   |
|   |   |
|   |   |

2. The figure below shows a logic circuit and its incomplete truth table. Complete the below truth table.



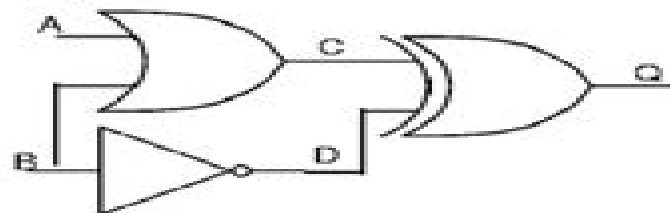
| A | B | C | Q |
|---|---|---|---|
| 0 | 0 |   |   |
| 0 | 1 |   |   |
| 1 | 0 |   |   |
| 1 | 1 |   |   |

3. The figure below shows a logic circuit and its incomplete truth table. Complete its truth table.



| A | B | C | D | Q |
|---|---|---|---|---|
| 0 | 0 |   |   |   |
| 0 | 1 |   |   |   |
| 1 | 0 |   |   |   |
| 1 | 1 |   |   |   |

4. The figure below shows a logic circuit and its incomplete truth table. Complete the below truth table.



| A | B | C | D | Q |
|---|---|---|---|---|
| 0 |   |   |   |   |
| 0 |   |   |   |   |
| 1 |   |   |   |   |
| 1 |   |   |   |   |

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**Yijin Wang**



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