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I Semester Diploma Examination, April/May-2021

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Ti	me:	3 Hou	rs [[Max	. Marks : 10	00
Inc	structi	lons :	(i) (ii)	One full question carries 20 marks.	1850	
1	(a)	Con (i) (ii)	Bir	SECTION - I the following : sary number 11011 to decimal number. cimal number 497 to Octal number.	.)]]	6
	(b)	(i) (ii)	Sig 1's	the decimal number (-49) in 8 bit binary form of in - Magnitude form complement form complement form		6
	(c)			ary equivalent ASCII code for the words		8
	AS			'A' is 65 in decimal, 'a' is 97 in decimal.		
2.	(a)			niversal gates with logic symbol, expressions, truth table.	-	8
	(b)			truth table for 3 input AND gate. e when the output of XOR and XNOR logic gates are high	h ~	8
				SECTION - II		
	(a)	Appl (i) (ii)	(A +	olean algebra rules / laws and prove, - B) (A + C) = A + BC + A (B + C) + B (B + C) = B + AC.		10
	(ъ)	Desig	en a f	ull adder circuit with truth table.		6
	(c)	Implement a comparator using Logic gates.				
	(a)			explain 4 : 1 multiplexer.		10
	(b)	Expla	in the	e construction of 4 bit synchronous counter with Truth t	able.	10
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