



Samsung Omnia User Manual Guide

Judd E. Hollander



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2020 Bernd Hoefflinger, 2011-12-15 The chips in present day cell phones already contain billions of sub 100 nanometer transistors By 2020 however we will see systems on chips with trillions of 10 nanometer transistors But this will be the end of the miniaturization because yet smaller transistors containing just a few control atoms are subject to statistical fluctuations and thus no longer useful We also need to worry about a potential energy crisis because in less than five years from now with current chip technology the internet alone would consume the total global electrical power This book presents a new sustainable roadmap towards ultra low energy femto Joule high performance electronics The focus is on the energy efficiency of the various chip functions sensing processing and communication in a top down spirit involving new architectures such as silicon brains ultra low voltage circuits energy harvesting and 3D silicon technologies Recognized world leaders from industry and from the research community share their views of this nanoelectronics future They discuss among other things ubiquitous communication based on mobile companions health and care supported by autonomous implants and by personal carebots safe and efficient mobility assisted by co pilots equipped with intelligent micro electromechanical systems and internet based education for a billion people from kindergarden to retirement This book should help and interest all those who will have to make decisions associated with future electronics students graduates educators and researchers as well as managers investors and policy makers Introduction Towards Sustainable 2020 Nanoelectronics From Microelectronics to Nanoelectronics The Future of Eight Chip Technologies Analog Digital Interfaces Interconnects and Transceivers Requirements and Markets for Nanoelectronics ITRS The International Technology Roadmap for Semiconductors

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