electrical module three past papers

		Answer any THREE questions from this section.
(n)	Dray	w a labelled block diagram of a microprocessor and state the function of each bloc
		bum o)
(6)	Expl	lain the function of each of the following microprocessor registers:
	(1)	status register;
	(iii)	memory address register;
	(iii)	index register.
		6 mark
(c)	Table	e 1 shows Intel 8085 program listing. For each instruction, state:
	(i)	addressing mode:
	(ii)	size of the machine code, in bytes.
		Table 1 (8 mark
		LXI H, 2040H
		ADD B
		STAX D INX D
(0)	Defin	se each of the following types of interrupts:
	(ii)	maskable, - polled.
-		
(9)	FOFE	direct memory access controller (DMAC):
	(1)	State two pins necessary for requesting and religuishing the bus from the CPU;
	(iii)	Explain the functions of each of the following registers.
		(I) command register; (II) mode register.
		(6 marks
		to make
(e)	(5)	Write an assembly language program segment to perform the following
(c)	6)	Write an assembly language program segment to perform the following:
(e)	0)	Write an assembly language program segment to perform the following: • read data from port A (81H) and port C (83H) of a programmable Input/output (P10) to form a 16-bit number;
(c)	(1)	 read data from port A (81H) and port C (83H) of a programmable Input/output
(c)	0)	 read data from port A (81H) and port C (83H) of a programmable Input/output (P10) to form a 16-bit number;
(c)	6)	 read data from port A (81H) and port C (83H) of a programmable Input/output (P10) to form a 16-bit number; store the 16-bit data into memory locations 1800H and 1801H end.
(e)		 read data from port A (8111) and port C (8311) of a programmable Input/output (P10) to form a 16-bit number; store the 16-bit data into memory locations 180011 and 1801 H
(0)		 read data from port A (8111) and port C (8311) of a programmable Input/output (P10) to form a 16-bit number; store the 16-bit data into memory locations 1800H and 1801 H end. A CPU clock frequency is 1 MHZ. Determine the number to be loaded into the internal timer register to achieve a single wave pulse every 50 µS. With the aid of a labelled diagram, show how a seven segment LFD display is
(0)	(ii).	 read data from port A (81H) and port C (83H) of a programmable Input/output (P10) to form a 16-bit number; store the 16-bit data into memory locations 1800H and 1801H end. A CPU clock frequency is 1 MHZ. Determine the number to be loaded into the internal timer register to achieve a single wave pulse every 50 µS.

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Ying-Ying Zheng

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Monthly Catalogue, United States Public Documents, 1985-07 Education Management, Education Theory and Education Application Yuanzhi Wang, 2011-10-09 This volume includes extended and revised versions of a set of selected papers from the 2011 2nd International Conference on Education and Educational Technology EET 2011 held in Chengdu China October 1 2 2011 The mission of EET 2011 Volume 2 is to provide a forum for researchers educators engineers and government officials involved in the general areas of education management education theory and education application to disseminate their latest research results and exchange views on the future research directions of these fields 133 related topic papers were selected into this volume All the papers were reviewed by 2 program committee members and selected by the volume editor Prof Yuanzhi Wang from Intelligent Information Technology Application Research Association Hong Kong The conference will bring together leading researchers engineers and scientists in the domain of interest We hope every participant can have a good opportunity to exchange their research ideas and results and to discuss the state of the art in the areas of the education management education theory and education application **A Collection of Technical Papers** Monthly Catalog of United States Government Publications ,1995 SAE Technical Paper Series ,1968 .1966 Online version Technical papers portion of the SAE Digital Library references thousands of SAE Technical Papers covering the latest advances and research in all areas of mobility engineering including ground vehicle aerospace off highway and manufacturing technology Sample coverage includes fuels and lubricants emissions electronics brakes restraint systems noise engines materials lighting and more Your SAE service includes detailed summaries complete documents in PDF plus **Reports and Documents** United States. Congress, document storage and maintenance Annual Department of Defense Bibliography of Logistics Studies and Related Documents United States. Defense Logistics Studies Information Exchange, 1975 **Resources in Education** .1995 Papers Presented at the AIAA/ASME/SAE/ASEE 25th Joint Propulsion Conference, 1989 **Decisions and Orders of the National Labor Relations Board** United States, National Labor Relations Board, 1981 **Daily Graphic** Elvis Aryeh, 2002-05-14 **Frontiers of Manufacturing and Design** Science Ran Chen, 2010-12-06 Selected peer reviewed papers from the 2010 International Conference on Frontiers of Manufacturing and Design Science ICFMD 2010 Chongging China December 11 12 2010 American Foreign Policy, **Current Documents** .1965 Official Gazette of the United States Patent and Trademark Office United States. Patent and Trademark Office, 2001 **Paper** ,2000 Film & Video Finder ,1997 **Ambient Intelligence** David Kevson, Mary Lou Maher, Norbert Streitz, Adrian David Cheok, Juan Carlos Augusto, Reiner Wichert, Gwenn Englebienne, Hamid Aghajan, Ben Kröse, 2011-11-11 This book constitutes the refereed proceedings of the Second International Joint Conference on Ambient Intelligence AmI 2011 held in Amsterdam The Netherlands in November 2011 The 58 revised full papers presented were carefully reviewed and selected from numerous submissions. The papers cover a wide range of topics such as

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