

2[Option ID=400040]  
3[Option ID=400041]  
4[Option ID=400042]

SE, No.379

Q6 ID:1055953

Which of the following is the primary source of water pollution in India's major rivers?

- (1) Disregulated small scale industries
- (2) Religious Practices
- (3) Agricultural run-off
- (4) Untreated Sewage

निम्नलिखित में से कौन सा भारत की प्रमुख नदियों में प्रदूषण का प्राथमिक स्रोत है?

- (1) अनियमित लघु उद्योग
- (2) धार्मिक प्रथाएँ
- (3) कृषीय वाह (रन-ऑफ)
- (4) अशोधित सीवेज

1[Option ID=400043]

2[Option ID=400044]

3[Option ID=400045]

4[Option ID=400046]

SE, No.379

Q6 ID:1055954

Which of the following is the best way to encourage divergent, creative thinking?

- (1) Engage in many activities simultaneously
- (2) Force yourself to think of something new and creative
- (3) Go for a walk, or engage in some other automatic activity
- (4) Stare at a blank sheet of paper until a new, innovative solution comes to mind.

निम्नलिखित में से कौन सा तरीका सबसे अच्छा है, जो विविध, रचनात्मक चिंतन को प्रोत्साहित करता है?

- (1) एक साथ कई गतिविधियाँ में संलग्न करना।
- (2) किसी नए और रचनात्मक विषय पर सोचने के लिए खुद को विवश करना।
- (3) पैदल चलना या किसी स्वचालित गतिविधि में संलग्न रहना।
- (4) किसी खाली कागज को धूर कर देखना जब तक कि कोई नया परिवर्तनकारी हल दिखाना में न आ जाए।

1[Option ID=400047]

2[Option ID=400048]

3[Option ID=400049]

4[Option ID=400050]

SE, No.390

Q6 ID:1055955

Which of the following statements about culture are correct.

- A. Culture persists through tradition
- B. Every animal, including man is called the culture-bearing animal
- C. Culture is the total social heritage
- D. Culture is an inherent quality of man.
- E. Culture is a system of learnt behavior

Choose the most appropriate answer from the options given below:

- (1) A B C Only
- (2) C D E Only
- (3) B C D Only
- (4) A C E Only

# Nmetal Work Question Paper

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web the i 2c module contains an independent i 2c master logic and a i 2c slave logic which generates interrupts based on their events in the multi master systems the user software is simply partitioned into the master controller and the slave controller when the i 2c master logic is active the slave logic also remains active detecting the

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web i2c master h this header contains i o definitions selection and timing clock speed settings used for the master i2c interface this file also includes prototypes of master i2c functions i2c master c this source file contains master i2c function implementations file

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web i2c module modes and features the i2c module provides the following operational modes and features master mode slave mode with byte nacking multi master mode dedicated receive and transmit buffers up to four dedicated slave address registers 1

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web the i<sup>2</sup>c master is byte oriented and interrupt based the number of interrupts generated is kept at a minimum by automatic handling of most incidents the software driver complexity and code size are reduced by auto triggering of operations and a special smart mode which can be enabled by the smart mode enable bit in the control b register

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web this example shows how the microcontroller configured in i<sup>2</sup>c host mode writes to and reads data from an mcp23008 8 bit i<sup>2</sup>c i/o expander client device addressed in 7 bit mode using interrupts

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web control of the i<sup>2</sup>c bus may be taken when the p bit is set or the bus is idle in firmware controlled master mode user code conducts all i<sup>2</sup>c bus operations based on start and stop bit condition detection start and stop condition detection is

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web lbidirectional i2c stands for inter integrated circuit communications i2c is implemented in the picmicro by a hardware module called the master synchronous serial port known as the mssp module

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web c bus is a two wire serial bus with multiple possible masters and multiple possible slaves connected to each other through two wires the two wires consists of a clock line scl and a data line sda with both lines being bi directional

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k40 and pic18 q10 product families have a master synchronous serial port

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web nov 7 2016 ds90003159b page 2 2017 microchip technology inc i2c protocol overview the i2c module follows the phillips i2c specification the module provides a bidirectional master slave syn chronous interface between the pic microcontroller and other i2c supported devices these devices are connected via a two wire serial bus

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web jun 23 2016 key features of the i2c module include the following independent master and slave logic multi master support which prevents message losses in arbitration detects 7 bit and 10 bit device addresses with configurable address masking in slave mode detects general call addresses as defined in the i2c protocol automatic sclx clock

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