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Department of Examinations, Sri Lanka

අධ්‍යාපන පොදු සහතික පත්‍ර (උසස් පෙළ) විභාගය, 2013 අගෝස්තු
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Two hours

Instructions:

- *Answer **all** the questions.*
- *Write your **Index Number** in the space provided in the answer sheet.*
- *Use of calculators is **not** allowed.*
- *Instructions are given on the back of the answer sheet. Follow those carefully.*
- *In each of the questions **1** to **50**, pick one of the alternatives from (1), (2), (3), (4), (5) which is **correct** or **most appropriate** and **mark your response with a cross** (×) in accordance with the instructions given on the back of the answer sheet.*

1. The Boolean expression $(x + y) . (x + z)$ simplifies to .....
   (1) x . (2) $x . (y + z)$ (3) $x . y . z$ (4) $x + y . z$ (5) $x + y + z$

2. Which of the following Boolean expressions represents the output (Q) of the circuit shown below? A and B are inputs.

   (1) $A' . B' + A . B'$
   (2) $A' . B' + A . B$
   (3) $A . B + A' . B'$
   (4) $A' . B + A . B'$
   (5) $A' . B + A' . B'$

3. A high speed temporary storage, which is a part of the microprocessor that holds data and instructions during the execution, is called .....
   (1) Registers. (2) RAM. (3) Virtual Memory.
   (4) EPROM. (5) Flash Memory.

4. Microprocessors are usually compared by their clock speed, measured in ..... or by their word size, measured in ..... that can be processed in a single clock cycle. Which of the following is most appropriate to fill the blanks in the above statement?
   (1) Bits, Megahertz (2) Bytes, Gigahertz (3) Gigahertz, Bytes
   (4) Megahertz, Bits (5) Seconds, Bits

5. Typically the cache memory is used to store .....
   (1) a large volume of data temporarily. (2) the least frequently accessed data permanently.
   (3) the least frequently accessed data temporarily. (4) the most frequently accessed data temporarily.
   (5) the most frequently accessed data permanently.

6. Sharing a single microprocessor among number of application programs using context switching is known as
   (1) Multi-user processing. (2) Multitasking. (3) Multiprocessing.
   (4) Batch processing. (5) Online processing.

7. Babbage's Difference Engine is based on .....
   (1) mechanical technology. (2) vacuum tube technology.
   (3) transistor technology. (4) Integrated Circuit (IC) technology.
   (5) Very Large Scale Integrated (VLSI) Circuit technology.

8. Which of the following components is located outside the microprocessor?
   (1) Arithmetic Logic Unit (ALU) (2) RAM
   (3) Control Unit (4) Registers
   (5) Level 1 cache memory

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