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[5668]-189

S.E. (Computer) (Second Semester) EXAMINATION, 2019 MICROPROCESSOR

(2015 PATTERN)

Time	2	Two	Hours

Maximum Marks : 50

- N.B. :- (1) Answer question Nos 1 or 2, 3 or 4, 5 or 6, 7 or 8.
 - (ii) Neat diagram must be drawn wherever necessary
 - (iii) Figures to the right indicate full marks
 - (iv) Assume suitable data if necessary
- 1. (a) List and explain coprocessor interface instructions of 80886
 - (b) With the help of diagram explain 80386 applications register set. [4]
 - (c) Explain how linear address 0080400A H will be translated into physical address using paging mechanism. Whether the address generated will be the same to linear address? [6]

Or

2. (a) Explain LEA and XLAT instructions-

[2]

(b) Draw and explain EFLAGs register of 60886

[4]

P.T.O.

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