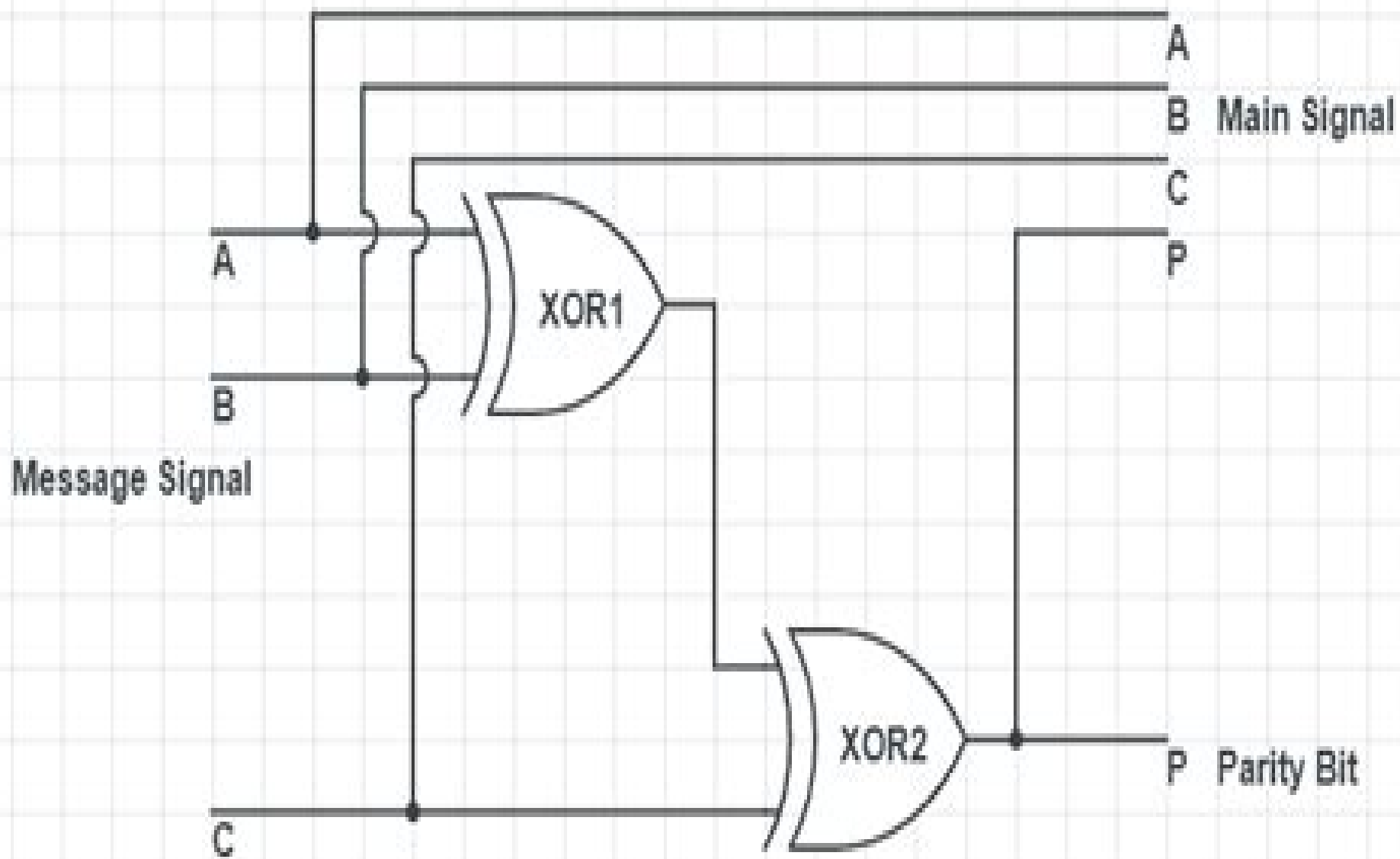


3-bit Even Parity Generator



Parity Bit Generator Circuit Diagram

Dr. P. Kannan, Mrs. M. Saraswathy

A red circular graphic with a gradient, appearing as a partial circle or a stylized arrow pointing to the right, located to the right of the authors' names.

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The Essentials of Computer Organization and Architecture Linda Null, Julia Lobur, 2006 Computer Architecture Software Engineering *Foundations of Digital Logic Design* Gideon Langholz, Abraham Kandel, Joe L. Mott, 1998 This text is intended for a first course in digital logic design at the sophomore or junior level for electrical engineering computer engineering and computer science programs as well as for a number of other disciplines such as physics and mathematics The book can also be used for self study or for review by practicing engineers and computer scientists not intimately familiar with the subject After completing this text the student should be prepared for a second advanced course in digital design switching and automata theory microprocessors or computer organization *Foundations of Digital Logic and Computer Systems* Dr. Ishaan Tamhankar, Dr. Sindhu Pandya, Dr. Yatin Patel, 2025-06-09 Foundations of Digital Logic and Computer Systems is a comprehensive introduction to the principles underlying modern computer technology beginning with the basics of binary numbers and Boolean algebra and progressing through combinational and sequential logic design The book explores how fundamental components like logic gates flip flops and multiplexers are used to construct memory units arithmetic logic units and control systems It bridges the gap between hardware and software by illustrating how digital logic forms the basis of computer architecture and how assembly language interacts with hardware Through clear explanations and practical examples the text builds a strong foundation for understanding how computers operate at their most fundamental level **Digital Electronics** Dr. P. Kannan, Mrs. M. Saraswathy, 2018-10-01 This book is extensively designed for the third semester ECE students as per Anna university syllabus R 2013 The following chapters constitute the following units Chapter 1 2 and Unit 1 Chapter 3 covers Unit 2 Chapter 4 and 5 covers Unit 3 Chapter 6 covers Unit 4 Chapter 7 covers Unit 5 Chapter 8 covers Unit 5 CHAPTER 1 Introduces the Number System binary arithmetic and codes CHAPTER 2 Deals with Boolean algebra simplification using Boolean theorems K map method Quine McCluskey method logic gates implementation of switching function using basic Logical Gates and Universal Gates CHAPTER 3 Describes the combinational circuits like Adder Subtractor Multiplier Divider magnitude comparator encoder decoder code converters Multiplexer and Demultiplexer CHAPTER 4 Describes with Latches Flip Flops Registers and Counters CHAPTER 5 Concentrates on the Analysis as well as design of synchronous sequential circuits Design of synchronous counters sequence generator and Sequence detector CHAPTER 6 Concentrates the Design as well as Analysis of Fundamental Mode circuits Pulse mode Circuits Hazard Free Circuits ASM Chart and Design of Asynchronous counters CHAPTER 7 Discussion on memory devices which includes ROM RAM PLA PAL Sequential logic devices and ASIC CHAPTER 8 Concentrate on the comparison operation and characteristics of RTL DTL TTL ECL and MOS families We have taken enough care to present the definitions and statements of basic laws and theorems problems with simple steps to make the students familiar with the fundamentals of Digital Design *Switching and Finite Automata Theory* Zvi Kohavi, Niraj K. Jha, 2010 Understand the structure behavior

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