(08 Marks)

### M.Tech. Degree Examination, June/July 2011 Real Time Operating Systems

Time: 3 hrs.

Max. Marks:100

#### Note: Answer any FIVE full questions.

- With diagrams explain Hard real time, Isochronal soft real-time and Soft isochronal service utilities.
   Differentiate between preemptive and non-preemptive scheduling.
   Explain thread safe reentrant function.
   (06 Marks)
- 2 a. Explain RM scheduling. Calculate the "utility of the CPU resource achievable" for the following system of tasks.

i)  $T_1 = 2$ ,  $T_2 = 5$ ,  $C_1 = 1$ ,  $C_2 = 1$ ii)  $T_1 = 2$ ,  $T_2 = 5$ ,  $C_1 = 1$ ,  $C_2 = 2$ 

Write scheduling diagram for each of the above. (98 Marks)

- With reference to RM LUB differentiate between i) sufficient ii) necessary and sufficient conditions for feasibility tests.
- With examples differentiate between deadline monotonic and earliest deadline first policies.
   (86 Marks)
- 3 a. Encode the following data using ECC/Hamming coding.

"1 1 0 0 0 1 0 0"

Prepare the table of coding.

b. Explain a simple pipeline with an example of stage overlap depth = 4. (96 Marks)

c. With diagram explain the three types of cache organizations. (96 Marks)

4 a. Write scheduling diagram for the following tasks to illustrate "DM policy succeeds when RM would fail".

 $T_1 = 2$ ;  $C_1 = 1$ ;  $D_1 = 2$   $T_2 = 5$ ;  $C_2 = 1$ ;  $D_2 = 3$   $T_3 = 2$ ;  $C_3 = 1$ ;  $D_1 = 7$  $T_4 = 13$ ;  $C_4 = 2$ ;  $D_4 = 15$ 

b. Explain the following:
i) Deadlock ii) Critical section iii) Priority inversion. (96 Marks)

c. Explain availability and quality of service. (06 Marks)

5 a. With a block diagram, explain different subsystems for stereo-vision tracking system.

b. Explain boot code and device drivers. (96 Marks)
c. Explain binary and mutex semaphores. (96 Marks)

c. Explain binary and mutex semaphores. (86 Marks)

a. What are exceptions, assert and single step debugging? How are they used? (88 Marks)

b. Differentiate between hardware and software breakpoints. (86 Marks)
c. Explain test access ports and trace ports. (86 Marks)

7 a. Explain reliability of dual string cross-strapped subsystem interconnection. (88 Marks)

b. With the help of a block diagram, explain low-end PIC micro MCU programmer. (66 Marks)
 c. Write a note on PIC 17CXX programming. (66 Marks)

- 8 a. Write a note on real time operating system. (66 Marks)
  - b. Device a strategy to derive the RM CUB for any given set of services S for which each services S<sub>i</sub> has an arbitrary C<sub>i</sub>, T<sub>i</sub> for the two cases.
     i) C<sub>1</sub> short enough to fit all the releases in T<sub>2</sub>.
     ii) C<sub>1</sub> too large to fit last release T<sub>2</sub>. (14 Marks)

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**Brendan G. Carr** 

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