

Bucknalla/ **OFDM_Verilog**



Example of OFDM in Python and Verilog

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Ofdm Verilog Code

Schmogrow, Rene Marcel

A red circular graphic with a gradient, appearing as a stylized arrow or a partial circle, located to the right of the author's name.

Ofdm Verilog Code:

Wireless OFDM Systems Marc Engels, 2005-12-15 From the reviews This book gives a comprehensive overview of the implementation of OFDM systems For those who study or work on broadband communication in a wireless multipath environment this book is a useful and easy to read reference Zongsen Wu Shaowen Song and Tianying Ji Physics and Computing Dept Wilfrid Laurier University ON Digital Design of Signal Processing Systems Shoab Ahmed Khan, 2011-07-28 Digital Design of Signal Processing Systems discusses a spectrum of architectures and methods for effective implementation of algorithms in hardware HW Encompassing all facets of the subject this book includes conversion of algorithms from floating point to fixed point format parallel architectures for basic computational blocks Verilog Hardware Description Language HDL SystemVerilog and coding guidelines for synthesis The book also covers system level design of Multi Processor System on Chip MPSoC a consideration of different design methodologies including Network on Chip NoC and Kahn Process Network KPN based connectivity among processing elements A special emphasis is placed on implementing streaming applications like a digital communication system in HW Several novel architectures for implementing commonly used algorithms in signal processing are also revealed With a comprehensive coverage of topics the book provides an appropriate mix of examples to illustrate the design methodology Key Features A practical guide to designing efficient digital systems covering the complete spectrum of digital design from a digital signal processing perspective Provides a full account of HW building blocks and their architectures while also elaborating effective use of embedded computational resources such as multipliers adders and memories in FPGAs Covers a system level architecture using NoC and KPN for streaming applications giving examples of structuring MATLAB code and its easy mapping in HW for these applications Explains state machine based and Micro Program architectures with comprehensive case studies for mapping complex applications The techniques and examples discussed in this book are used in the award winning products from the Center for Advanced Research in Engineering CARE Software Defined Radio 10 Gigabit VoIP monitoring system and Digital Surveillance equipment has respectively won APICTA Asia Pacific Information and Communication Alliance awards in 2010 for their unique and effective designs **Advanced Information Networking and Applications** Leonard Barolli, Farookh Hussain, Tomoya Enokido, 2022-03-30 This book covers the theory design and applications of computer networks distributed computing and information systems Networks of today are going through a rapid evolution and there are many emerging areas of information networking and their applications Heterogeneous networking supported by recent technological advances in low power wireless communications along with silicon integration of various functionalities such as sensing communications intelligence and actuations is emerging as a critically important disruptive computer class based on a new platform networking structure and interface that enable novel low cost and high volume applications Several of such applications have been difficult to realize because of many interconnections problems To fulfill their large range of

applications different kinds of networks need to collaborate and wired and next generation wireless systems should be integrated in order to develop high performance computing solutions to problems arising from the complexities of these networks The aim of the book *Advanced Information Networking and Applications* is to provide the latest research findings innovative research results methods and development techniques from both theoretical and practical perspectives related to the emerging areas of information networking and applications

OFDM Wireless LANs John Terry, Juha Heiskala, 2002
Annotation Deploy and optimize your wireless LAN using the new standard for broadband wireless communication OFDM A comprehensive reference written by two experts who helped create the OFDM specifications A detailed practical guide to OFDM WLANs does not exist requiring readers to seek out multiple sources of information such as white papers and research notes Detailed explanations of the concepts and algorithms behind OFDM context that is missing from the two OFDM books currently available This book explains OFDM WLAN basics including components of OFDM and multicarrier WLAN standards It provides a practical approach to OFDM by including software and hardware examples and detailed implementation explanations

OFDM Multicarrier Wireless Networks A Practical Approach defines and explains the mathematical concepts behind OFDM necessary for successful OFDM WLAN implementations Juha Heiskala is a research engineer at Nokia Research Center in Irving TX Heiskala is active in the IEEE 802.11 standards bodies and has been tasked with developing the 802.11a system simulation on several software platforms He is the inventor co inventor of three pending patents in the area of OFDM LANs and co designed with Dr John Terry the modulation and coding scheme for achieving 100 Mbps speeds within currently allocated band specifications for OFDM WLANs John Terry Ph D is a senior research engineer at Nokia Research Center He is currently managing the OFDM modulation and coding project in the HSA group Dr Terry has published several white papers given numerous presentations on wireless communications and generated four patents related to OFDM WLANs He has 10 years of experience working in wireless communications including tenures at NASA Glen Research Center and Texas Instruments

Embedded Computer Systems: Architectures, Modeling, and Simulation Mladen Berekovic, Nikitas Dimopoulos, Stephan Wong, 2008-07-07 This book constitutes the refereed proceedings of the 8th International Workshop on Systems Architectures Modeling and Simulation SAMOS 2008 held in Samos Greece in July 2008 The 24 revised full papers presented together with a contemplative keynote and additional papers of two special workshop sessions were carefully reviewed and selected from 62 submissions The papers are organized in topical sections on architecture new frontiers SoC application specific contributions system level design for heterogeneous systems programming multicores sensors and sensor networks and systems modeling and design

Real-time Digital Signal Processing for Software-defined Optical Transmitters and Receivers Schmogrow, Rene Marcel, 2014-11-21 A software defined optical Tx is designed and demonstrated generating signals with various formats and pulse shapes in real time Special pulse shapes such as OFDM or Nyquist signaling were utilized resulting in a highly efficient usage of the available fiber channel

bandwidth This was achieved by parallel data processing with high end FPGAs Furthermore highly efficient Rx algorithms for carrier and timing recovery as well as for polarization demultiplexing were developed and investigated *Proceedings*, 2003 **The ... IEEE International Symposium on Personal, Indoor, and Mobile Radio Communications** IEEE International Symposium on Personal, Indoor, and Mobile Radio Communications, 1995 Communication, Devices, and Computing Jaydeb Bhaumik, Indrajit Chakrabarti, Bishnu Prasad De, Banibrata Bag, Surajit Mukherjee, 2018-04-07 This book provides insights into the First International Conference on Communication Devices and Computing ICCDC 2017 which was held in Haldia India on November 23 2017 It covers new ideas applications and the experiences of research engineers scientists industrialists scholars and students from around the globe The proceedings highlight cutting edge research on communication electronic devices and computing and address diverse areas such as 5G communication spread spectrum systems wireless sensor networks signal processing for secure communication error control coding printed antennas analysis of wireless networks antenna array systems analog and digital signal processing for communication systems frequency selective surfaces radar communication and substrate integrated waveguide and microwave passive components which are key to state of the art innovations in communication technologies **Inventive Computation and Information Technologies** S. Smys, Valentina Emilia Balas, Ram Palanisamy, 2022-01-18 This book is a collection of best selected papers presented at the International Conference on Inventive Computation and Information Technologies ICICIT 2021 organized during 12-13 August 2021 The book includes papers in the research area of information sciences and communication engineering The book presents novel and innovative research results in theory methodology and applications of communication engineering and information technologies **Embedded Microprocessor System Design using FPGAs** Uwe Meyer-Baese, 2021-03-15 This textbook for courses in Embedded Systems introduces students to necessary concepts through a hands on approach It gives a great introduction to FPGA based microprocessor system design using state of the art boards tools and microprocessors from Altera Intel and Xilinx HDL based designs soft core parameterized cores Nios II and MicroBlaze and ARM Cortex A9 design are discussed compared and explored using many hand on designs projects Custom IP for HDMI coder Floating point operations and FFT bit swap are developed implemented tested and speed up is measured Downloadable files include all design examples such as basic processor synthesizable code for Xilinx and Altera tools for PicoBlaze MicroBlaze Nios II and ARMv7 architectures in VHDL and Verilog code as well as the custom IP projects Each Chapter has a substantial number of short quiz questions exercises and challenging projects Explains soft parameterized and hard core systems design tradeoffs Demonstrates design of popular KCPSM6 8 Bit microprocessor step by step Discusses the 32 Bit ARM Cortex A9 and a basic processor is synthesized Covers design flows for both FPGA Market leaders Nios II Altera Intel and MicroBlaze Xilinx system Describes Compiler Compiler Tool development Includes a substantial number of Homework s and FPGA exercises and design projects in each chapter Pervasive Ambient Communication for Internet of

Things Wei Gong,Yimeng Huang,Jia Zhao,Jiangchuan Liu,2023-08-23 This book covers several essential aspects of pervasive ambient backscatter communication one of the most cutting edge technologies for Internet of Things It begins with introductory Part I which presents visions basic concepts principles and paradigms of ambient backscatter Also a taxonomy of state of the art ambient backscatter systems is provided from the perspective of the OSI model Part II and III study how ambient backscatter systems work on the communication and networking levels Specifically Part II discusses in detail how to make use of ambient WiFi signals to provide high throughput backscatter communications with WiFi 1 chapter 5 WiFi 2 3 chapter 4 WiFi 4 chapter 3 WiFi 5 6 chapter 6 standards Further Part III includes several of the most advanced ambient backscatter network solutions which are made possible by the first multi hop backscatter chapter 7 first backscatter mesh chapter 8 and multiprotocol backscatter chapter 9 On top of reliable communication and networks we propose two novel applications that are thought impossible before lightweight spatial sound recording over the air chapter 10 and self powered wireless wearables for healthcare chapter 11 To conclude the monograph we point out critical challenges for realizing the vision of pervasive backscatter IoTs and potential directions of ambient backscatter applications The book provides an in depth understanding of ambient backscatter technologies In particular we mainly take ubiquitous WiF signals as the communication sources and adopt a top down approach to introduce three crucial subjects WiFi backscatter communication ambient backscatter network and self powered application systems For each subject we carefully divide it into several relatively independent topics which come with the latest advances in pervasive backscatter and include extensive discussions of closely related state of the art methodologies

The Digital Consumer Technology Handbook Amit Dhir,2004-04-30 The consumer electronics market has never been as awash with new consumer products as it has over the last couple of years The devices that have emerged on the scene have led to major changes in the way consumers listen to music access the Internet communicate watch videos play games take photos operate their automobiles even live Digital electronics has led to these leaps in product development enabling easier exchange of media cheaper and more reliable products and convenient services This handbook is a much needed comprehensive engineering guide to the dynamic world of today s digital consumer electronics It provides complete details on key enabling technologies standards delivery and reception systems products appliances and networking systems Each chapter follows a logical progression from a general overview of each device to market dynamics to the core technologies and components that make up that particular product The book thoroughly covers all of the key digital consumer product categories digital TV digital audio mobile communications devices gaming consoles DVD players PCs and peripherals display devices digital imaging devices web terminals and pads PDAs and other handhelds screenphones videophones telematics devices eBooks and readers and many other current and future products To receive a FREE daily newsletter on displays and consumer electronics go to <http://www.displaydaily.com> Surveys crucial engineering information for every digital consumer product category including cell phones digital TVs digital cameras PDAs and many

more the only reference available to do so Has extremely broad market appeal to embedded systems professionals including engineers programmers engineering managers marketing and sales personnel 1 000 000 potential readers Helps engineers and managers make the correct design decisions based on real world data Konzipierung und Realisierung einer Ethernet-Anbindung für OFDM-Funkübertragungssysteme Ronny Zavrtak,2005-09-09 Inhaltsangabe Einleitung Diese Arbeit wurde im Institut für angewandte Funksystemtechnik IAF in Braunschweig im Zeitraum von September 2004 bis Januar 2005 angefertigt Die Firma IAF führt anwendungsorientierte Forschungs und Entwicklungsprojekte im Bereich der digitalen Funkübertragung durch Für den Aufbau von Experimentalsystemen für Forschungszwecke im Bereich Orthogonal Frequency Division Multiplex OFDM Funkübertragungssysteme wurde eine universelle Field Programmable Gate Array FPGA Plattform entwickelt Diese Plattform basiert auf Bausteinen der Firma Xilinx Im Rahmen zukünftiger Projekte sollen sowohl der Media Access Control MAC Layer als auch der Physical PHY Layer für ein Ethernet Interface implementiert werden Der Focus soll verstärkt auf integrierbare Lösungen wie IP Cores gelegt werden Dabei wird die nach wirtschaftlichen Gesichtspunkten zweckmäßigste Lösung gesucht Konzeption und Integration eines Ethernet Interface in einen Virtex2Pro FPGA auf dem vorhandenen Prototyping Board unter Abwägung der Nutzung eines Intellectual Property IP Core der die Aufgabe des Netzwerkcontroller mit MAC und PHY Funktionen übernimmt gegenüber einer Hardwarelösung auf einer Aufsteckplatine Das Entwicklungsboard der Firma IAF soll als OFDM Modem für Forschungszwecke eingesetzt werden Die Ethernetschnittstelle stellt dann die Verbindung zur Außenwelt her Mit Hilfe der Schnittstelle kann das Board von einem gewöhnlichen Rechner mit 100MBit/s Ethernetanbindung Daten über UTP Cat 5 Kabel empfangen und senden Dabei ist es möglich alle Daten transparent weiterzuleiten So kann bei der Kopplung zweier Boards eine Ende zu Ende Verbindung zweier Rechner hergestellt werden die beispielsweise IP Daten austauschen sollen Die Daten werden dafür im Full Duplex Mode übertragen Gang der Untersuchung Das zweite Kapitel beschreibt das vorwiegend für Netzworkkommunikation verwendete Open Systems Interconnection OSI Referenz Modell RM und die in dieser Arbeit relevanten Schichten Ausserdem wird der Bezug zum Standard IEEE 802.3 hergestellt Darauf aufbauend werden Realisierungsvarianten für die Integration der Ethernetschnittstelle mit ihren Unterschieden aufgezeigt In Kapitel 3 wird konkret eine Umsetzungsvariante bestimmt die nachfolgend realisiert wird Dazu erfolgt die Auswahl der Hard- und Firmware Kapitel 4 enthält die methodische Vorgehensweise bei der Entwicklung eines HDL Modells anhand der verwendeten Hilfsmittel **International Conference on Intelligent Data Communication Technologies and Internet of Things (ICICI) 2018** Jude Hemanth,Xavier Fernando,Pavel Lafata,Zubair Baig,2018-12-20 This book discusses data communication and computer networking communication technologies and the applications of IoT Internet of Things big data cloud computing and healthcare informatics It explores examines and critiques intelligent data communications and presents inventive methodologies in communication technologies and IoT Aimed at researchers and academicians who need to understand the importance of data

communication and advanced technologies in IoT it offers different perspectives to help readers increase their knowledge and motivates them to conduct research in the area highlighting various innovative ideas for future research

Run-time Reconfigurable Constant Multiplication on Field Programmable Gate Arrays Möller, Konrad, 2017 This book addresses the question how run time reconfigurable constant multipliers RCMs can be efficiently implemented on field programmable gate arrays FPGAs RCMs calculate the multiplication of an input number by one out of several constants which can be selected during run time This is important as constant multiplication is an essential operation in digital signalprocessing DSP applications The evaluation of RCMs is done by considering reconfiguration using reconfigurable look up tables LUTs reconfiguration using multiplexers MUXs and Partial Reconfiguration PR This book contributes two new methods to generate RCMs using the first two reconfiguration principles First a LUT based constant multiplier is extended to be reconfigurable Second optimized constant multipliers without reconfiguration are fused using MUXs Moreover a general post optimization for MUX based RCMs is proposed Finally the design space produced in this way is analyzed using synthesis experiments The contributed methods provide important trade off points in the design space of RCMs on FPGAs

Cognitive Radio, Software Defined Radio, and Adaptive Wireless Systems Hüseyin Arslan, 2007-09-05 Today s wireless services have come a long way since the roll out of the conventional voice centric cellular systems The demand for wireless access in voice and high rate data multi media applications has been increasing New generation wireless communication systems are aimed at accommodating this demand through better resource management and improved transmission technologies The interest in increasing Spectrum Access and improving Spectrum Efficiency combined with both the introduction of Software Defined Radios and the realization that machine learning can be applied to radios has created new intriguing possibilities for wireless radio researchers This book is aimed to discuss the cognitive radio software defined radio SDR and adaptive radio concepts from several aspects Cognitive radio and cognitive networks will be investigated from a broad aspect of wireless communication system enhancement while giving special emphasis on better spectrum utilization Applications of cognitive radio SDR and cognitive radio architectures spectrum efficiency and soft spectrum usage adaptive wireless system design measurements and awareness of various parameters including interference temperature and geo location information are some of the important topics that will be covered in this book Cognitive Radio Software Defined Radio and Adaptive Wireless Systems is intended to be both an introductory technology survey tutorial for beginners and an advanced mathematical overview intended for technical professionals in the communications industry technical managers and researchers in both academia and industry

Konzeption und Realisierung einer Ethernet-Anbindung für

OFDM-Funkübertragungssysteme Ronny Zavrtak, 2007 Diplomarbeit aus dem Jahr 2005 im Fachbereich Ingenieurwissenschaften Nachrichtentechnik Kommunikationstechnik Note 1 0 Wests chsische Hochschule Zwickau Standort Zwickau 25 Quellen im Literaturverzeichnis Sprache Deutsch Abstract Diese Arbeit wurde im Institut f r angewandte

Funksystemtechnik IAF in Braunschweig im Zeitraum von September 2004 bis Januar 2005 angefertigt Die Firma IAF führt anwendungsorientierte Forschungs und Entwicklungsprojekte im Bereich der digitalen Funkübertragung durch Für den Aufbau von Experimentalsystemen für Forschungszwecke im Bereich Orthogonal Frequency Division Multiplex OFDM Funkübertragungssysteme wurde eine universelle Field Programmable Gate Array FPGA Plattform entwickelt Diese Plattform basiert auf Bausteinen der Firma Xilinx Im Rahmen zukünftiger Projekte sollen sowohl der Media Access Control MAC Layer als auch der Physical PHY Layer für ein Ethernet Interface implementiert werden Der Fokus soll verstärkt auf integrierbare Lösungen wie IP Cores gelegt werden Dabei wird die nach wirtschaftlichen Gesichtspunkten zweckmäßigste Lösung gesucht Zielstellung ist die Konzeption und Integration eines Ethernet Interface in einen Virtex2Pro FPGA auf dem vorhandenen Prototyping Board unter Abwägung der Nutzung eines Intellectual Property IP Core der die Aufgabe des Netzwerkcontroller mit MAC und PHY Funktionen übernimmt gegenüber einer Hardwarelösung auf einer Aufsteckplatine Das Entwicklungsboard der Firma IAF soll als OFDM Modem für Forschungszwecke eingesetzt werden Die Ethernetschnittstelle stellt dann die Verbindung zur Außenwelt her Der momentan am weitesten verbreitete Standard für lokale Netze ist Ethernet Er geht auf gemeinsame Spezifikationen von Intel DEC und Xerox zurück Der Name Ethernet weist noch auf die ersten Funknetze ALOHA hin Die Datenübertragung erfolgt mit dem CSMA/CD Verfahren Das Ethernet besteht physikalisch aus verschiedenen Typen von 50 Ohm Koaxkabeln oder paarweise verdrehten Leitungen Twisted Pair Glasfasern oder anderen Medien Die Datenrate beträgt typisch 100MBit/s für 10MBit/s gegenüber 100 und 1000MBit/s zukünftig 10GBit/s

SystemC Wolfgang Müller, Wolfgang Rosenstiel, Jürgen Ruf, 2007-05-08 SystemC has received a wide acceptance by users and tool vendors as the next generation system description language in order to deal with higher levels of abstraction for complex SoC designs SystemC Methodologies and Applications gives a comprehensive survey on the state of the art of SystemC in industry and research Organised into 11 self contained chapters selected SystemC experts present their approaches in the domains of modelling analysis synthesis Their contributions range from mixed signal and discrete system to embedded software The chapters give a broad overview of recent advances in SystemC methodologies and applications and are mainly based on presentations given at the European SystemC User Group meetings

Advances in Computing and Network Communications Sabu M. Thampi, Erol Gelenbe, Mohammed Atiquzzaman, Vipin Chaudhary, Kuan-Ching Li, 2021-04-20 This book constitutes the thoroughly refereed post conference proceedings of the 4th International Conference on Computing and Network Communications CoCoNet 20 October 14-17 2020 Chennai India The papers presented were carefully reviewed and selected from several initial submissions The papers are organized in topical sections on Signal Image and Speech Processing Wireless and Mobile Communication Internet of Things Cloud and Edge Computing Distributed Systems Machine Intelligence Data Analytics Cybersecurity Artificial Intelligence and Cognitive Computing and Circuits and Systems The book is directed to the researchers and scientists engaged in various fields of computing and network communication domains

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