

2-7 Reteaching

Solving Proportions

A proportion is an equation that states that two ratios are equal. If a quantity in a proportion is unknown, you can solve a proportion to find the unknown quantity as shown below.

Problem

What is the solution of $\frac{3}{4} = \frac{x}{14}$?

There are two methods for solving proportions—using the Multiplication Property of Equality and the Cross Products Property.

- 1) The Multiplication Property of Equality says that you can multiply both sides of an equation by the same number without changing the value.

$$\begin{aligned}\frac{3}{4} &= \frac{x}{14} \\ 14\left(\frac{3}{4}\right) &= 14\left(\frac{x}{14}\right) \\ \frac{42}{4} &= x \\ 10.5 &= x\end{aligned}$$

To isolate x , multiply each side by 14.

Simplify.

Divide 42 by 4.

- 2) The Cross Products Property says that you can multiply diagonally across the proportion and these products are equal.

$$\begin{aligned}\frac{3}{4} &= \frac{x}{14} \\ (4)(x) &= (3)(14) \\ 4x &= 42 \\ \frac{4x}{4} &= \frac{42}{4} \\ x &= 10.5\end{aligned}$$

Multiply diagonally across the proportion.

Multiply.

To isolate x , divide each side by 4.

Simplify.

Real world situations can be modeled using proportions.

Problem

A bakery can make 6 dozen donuts every 21 minutes. How many donuts can the bakery make in 2 hours?

A proportion can be used to answer this question. It is key for you to set up the proportion with matching units in both numerators and both denominators.

For this problem, you know that 2 hours is 120 minutes and 6 dozen is 72 donuts.

Correct:

$$\frac{72 \text{ donuts}}{21 \text{ min}} = \frac{x \text{ donuts}}{120 \text{ min}}$$

Incorrect:

$$\frac{72 \text{ donuts}}{21 \text{ min}} = \frac{120 \text{ min}}{x \text{ donuts}}$$

Prentice Hall Algebra 2 Chapter Answers

Gareth Williams



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Student Solutions Manual to Accompany Linear Algebra with Applications Gareth Williams,2010-03-18

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on the Internet via anonymous ftp further adding to the value of this title **Engineering Digital Design** Richard F. Tindler, 2000-01-18 Engineering Digital Design Second Edition provides the most extensive coverage of any available textbook in digital logic and design The new REVISED Second Edition published in September of 2002 provides 5 productivity tools free on the accompanying CD ROM This software is also included on the Instructor's Manual CD ROM and complete instructions accompany each software program In the REVISED Second Edition modern notation combines with state of the art treatment of the most important subjects in digital design to provide the student with the background needed to enter industry or graduate study at a competitive level Combinatorial logic design and synchronous and asynchronous sequential machine design methods are given equal weight and new ideas and design approaches are explored The productivity tools provided on the accompanying CD are outlined below

- 1 EXL Sim2002 logic simulator EXL Sim2002 is a full featured interactive schematic capture and simulation program that is ideally suited for use with the text at either the entry or advanced level of logic design Its many features include drag and drop capability rubber banding mixed logic and positive logic simulations macro generation individual and global or randomized delay assignments connection features that eliminate the need for wire connections schematic page sizing and zooming waveform zooming and scrolling a variety of printout capabilities and a host of other useful features
- 2 BOOZER logic minimizer BOOZER is a software minimization tool that is recommended for use with the text It accepts entered variable EV or canonical 1's and 0's data from K maps or truth tables with or without don't cares and returns an optimal or near optimal single or multi output solution It can handle up to 12 functions Boolean functions and as many inputs when used on modern computers
- 3 ESPRESSO II logic minimizer ESPRESSO II is another software minimization tool widely used in schools and industry It supports advanced heuristic algorithms for minimization of two level multi output Boolean functions but does not accept entered variables It is also readily available from the University of California Berkeley 1986 VLSI Tools Distribution
- 4 ADAM design software ADAM for Automated Design of Asynchronous Machines is a very powerful productivity tool that permits the automated design of very complex asynchronous state machines all free of timing defects The input files are state tables for the desired state machines The output files are given in the Berkeley format appropriate for directly programming PLAs ADAM also allows the designer to design synchronous state machines timing defect free The options include the lumped path delay LPD model or NESTED CELL model for asynchronous FSM designs and the use of D FLIP FLOPs for synchronous FSM designs The background for the use of ADAM is covered in Chapters 11 14 and 16 of the REVISED 2nd Edition
- 5 A OPS design software A OPS for Asynchronous One hot Programmable Sequencers is another very powerful productivity tool that permits the design of asynchronous and synchronous state machines by using a programmable sequencer kernel This software generates a PLA or PAL output file in Berkeley format or the VHDL code for the automated timing defect free designs of the following
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