

[4317]-22

F.Y. B.Sc.

## ELECTRONIC SCIENCE

EL1 - T2 : Principles of Digital Electronics  
(2008 Pattern) (Paper - II)

Time : 3 Hours]

[Max. Marks : 80

Instructions to the candidates:

- 1) All questions are compulsory.
- 2) Neat labelled diagrams must be drawn wherever necessary.
- 3) Use calculator and log table is allowed.
- 4) Figures to the right indicate full marks.

**Q1)** Answer the following questions in brief :**[16]**

- a) What is the radix used in the case of binary and hexadecimal?
- b) Why NAND and NOR gates are called as universal gate?
- c) Draw logic diagram for logic equation  $Y = (A + B).C$
- d) Subtract (1001) from (1100) using the 1's complement method.
- e) Define :
  - i) Combinational logic circuit.
  - ii) Sequential logic circuit.
- f) Draw logic diagram and write truth table for T - flip - flop.
- g) Define the terms noise immunity and noise margin.
- h) State the basic principle of operation of static RAM cell and Dynamic RAM cell.

**Q2)** Answer any four of the following :**[16]**

- a) Convert the following.
  - i)  $(101110)_2 = (\dots\dots\dots)_{Gray}$
  - ii)  $(0110)_{Gray} = (\dots\dots\dots)_2$
- b) Draw logic diagram of two input OR gate using diode. Explain its action.
- c) Simplify following expression using K - Map.  
$$Y = \overline{A}BCD + A\overline{B}CD + ABC\overline{D} + ABCD$$
- d) With the neat diagram explain working of parallel 4 - bit binary subtractor.
- e) Draw neat diagram of key - board encoder. Explain its operation with truth table.
- f) Explain working of clocked R - S flip - flop with logic diagram and truth table.

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**Advances and Applications in Computer Science, Electronics, and Industrial Engineering** Marcelo V. Garcia,Félix Fernández-Peña,Carlos Gordón-Gallegos,2022-05-25 This book presents the proceedings of the 3rd Conference on Computer Science Electronics and Industrial Engineering CSEI 2021 held in Ambato in October 2021 with participants from 10 countries and guest speakers from Chile Colombia Brasil Spain Portugal and United States Featuring 20 peer reviewed papers it discusses topics such as the use of metaheuristics for non deterministic problem solutions software architectures for supporting e government initiatives and the use of electronics in e learning and industrial environments It also includes contributions illustrating how new approaches to these converging research areas are impacting the

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## **Table of Contents Past Nindustrial Electronics Question Papers**

1. Understanding the eBook Past Nindustrial Electronics Question Papers
  - The Rise of Digital Reading Past Nindustrial Electronics Question Papers
  - Advantages of eBooks Over Traditional Books
2. Identifying Past Nindustrial Electronics Question Papers
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Past Nindustrial Electronics Question Papers
  - User-Friendly Interface
4. Exploring eBook Recommendations from Past Nindustrial Electronics Question Papers
  - Personalized Recommendations
  - Past Nindustrial Electronics Question Papers User Reviews and Ratings
  - Past Nindustrial Electronics Question Papers and Bestseller Lists

5. Accessing Past Nindustrial Electronics Question Papers Free and Paid eBooks
  - Past Nindustrial Electronics Question Papers Public Domain eBooks
  - Past Nindustrial Electronics Question Papers eBook Subscription Services
  - Past Nindustrial Electronics Question Papers Budget-Friendly Options
6. Navigating Past Nindustrial Electronics Question Papers eBook Formats
  - ePub, PDF, MOBI, and More
  - Past Nindustrial Electronics Question Papers Compatibility with Devices
  - Past Nindustrial Electronics Question Papers Enhanced eBook Features
7. Enhancing Your Reading Experience
  - Adjustable Fonts and Text Sizes of Past Nindustrial Electronics Question Papers
  - Highlighting and Note-Taking Past Nindustrial Electronics Question Papers
  - Interactive Elements Past Nindustrial Electronics Question Papers
8. Staying Engaged with Past Nindustrial Electronics Question Papers
  - Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Past Nindustrial Electronics Question Papers
9. Balancing eBooks and Physical Books Past Nindustrial Electronics Question Papers
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Past Nindustrial Electronics Question Papers
10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
11. Cultivating a Reading Routine Past Nindustrial Electronics Question Papers
  - Setting Reading Goals Past Nindustrial Electronics Question Papers
  - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Past Nindustrial Electronics Question Papers
  - Fact-Checking eBook Content of Past Nindustrial Electronics Question Papers
  - Distinguishing Credible Sources
13. Promoting Lifelong Learning



- Utilizing eBooks for Skill Development
  - Exploring Educational eBooks
14. Embracing eBook Trends
- Integration of Multimedia Elements
  - Interactive and Gamified eBooks

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