

[4317]-22

F.Y. B.Sc.

ELECTRONIC SCIENCE

EL1 - T2 : Principles of Digital Electronics
(2008 Pattern) (Paper - II)

Time : 3 Hours]

[Max. Marks : 80

Instructions to the candidates:

- 1) All questions are compulsory.
- 2) Neat labelled diagrams must be drawn wherever necessary.
- 3) Use calculator and log table is allowed.
- 4) Figures to the right indicate full marks.

Q1) Answer the following questions in brief :**[16]**

- a) What is the radix used in the case of binary and hexadecimal?
- b) Why NAND and NOR gates are called as universal gate?
- c) Draw logic diagram for logic equation $Y = (A + B).C$
- d) Subtract (1001) from (1100) using the 1's complement method.
- e) Define :
 - i) Combinational logic circuit.
 - ii) Sequential logic circuit.
- f) Draw logic diagram and write truth table for T - flip - flop.
- g) Define the terms noise immunity and noise margin.
- h) State the basic principle of operation of static RAM cell and Dynamic RAM cell.

Q2) Answer any four of the following :**[16]**

- a) Convert the following.
 - i) $(101110)_2 = (\dots\dots\dots)_{Gray}$
 - ii) $(0110)_{Gray} = (\dots\dots\dots)_2$
- b) Draw logic diagram of two input OR gate using diode. Explain its action.
- c) Simplify following expression using K - Map.
$$Y = \overline{A}BCD + A\overline{B}CD + ABC\overline{D} + ABCD$$
- d) With the neat diagram explain working of parallel 4 - bit binary subtractor.
- e) Draw neat diagram of key - board encoder. Explain its operation with truth table.
- f) Explain working of clocked R - S flip - flop with logic diagram and truth table.

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Industrial Electronics N3 Johann Kraft,2000 **Innovative Algorithms and Techniques in Automation, Industrial Electronics and Telecommunications** Tarek Sobh,Khaled Elleithy,Ausif Mahmood,Mohamed Karim,2007-09-04

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