

- One (1) 1n Farad Capacitor
- 741 Op-Amp
- Multisim Version 11, by National Instruments
- Oscilloscope

III. CIRCUIT DIAGRAM:

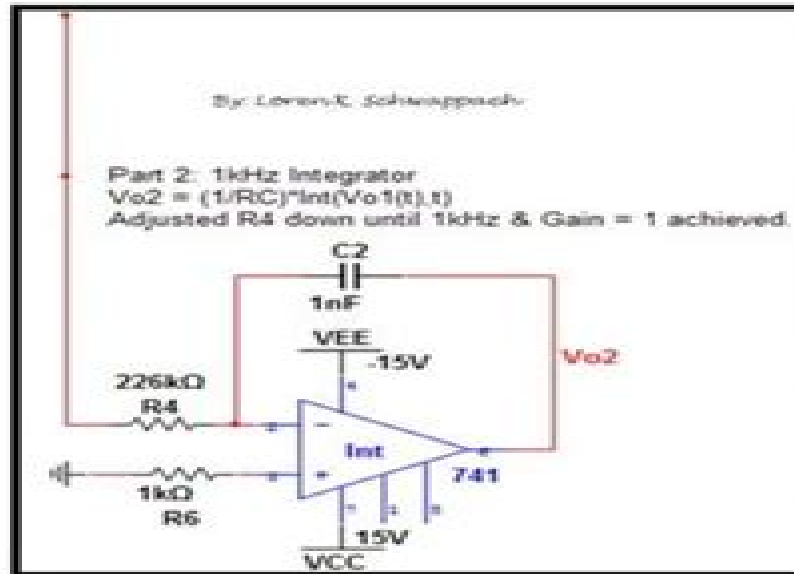


Figure 4: Multisim 1 KHz Integrator, designed the same as the integrator in lab #1 but using different RC values.

IV. RESULTS:

As shown by Figure 5 the integrator correctly produced a 15 Vp signal oscillating at 1k Hertz. The oscillator and integrator outputs are shown side by side on the oscilloscope display Figure 6.

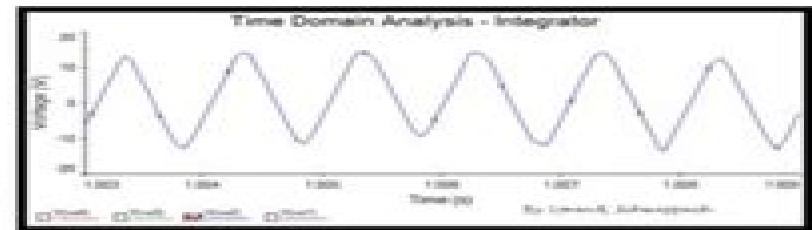


Figure 5: Multisim transient analysis results of integrator output.

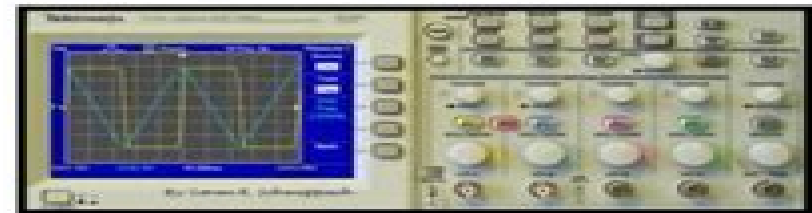


Figure 6: Oscilloscope results of integrator output (blue) and oscillator output yellow.

C. PART 3 – STEP GENERATOR AND RESET CIRCUIT

The step generator took in the 1k Hertz oscillator square wave output and generated a rising step pattern. The number of steps produced was dependent upon the input voltage provided to the reset circuit (comparator). The two diodes (D1 and D2) allowed rectification of the oscillator output and ensured that the step generators output increased each cycle of the oscillator until finally reset by the reset circuit.

The reset circuit compared the step generators output with an externally provided (via voltage divider) bias to allow the resetting of transistor Q2. When transistor Q2 is conducting it is in the reset condition, and provides a path for current around capacitor C4 (not yet shown). When C4 discharges the output and input of the step generator equalize resetting the step generator.

I. CALCULATIONS:

No calculations were required, because the step generator and reset circuitry was provided by the instructor.

However, modifications to the step generator and reset circuits were required to component availability (capacitors). Finally the instructors design was improved by using variable resistors (potentiometers) in Multisim,

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RS Peters



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