



# Order Guide Q7 2009

**KJ Lindholm-Leary**



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web jul 9 2023 the following diagram shows a simple on off delay timer circuit in this circuit the timer is a 555 timer chip the relay is a dpdt relay the power supply is a 9v battery the timer is configured in the astable mode this means that it will generate a square wave output

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web this type of positive feedback is also called hysteresis and provides a fast transition from the on to off state once turning off starts the off delay timing is mostly a function of c1 and r6 the exact delay is a bit hard to predict because it depends on the gain of the transistors particularly of q1

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