- OR (a) List the various losses occuring in DC machine. Also derive the relationship 07 O.3between armature power developed (Pa) and the output power (P) for both- DC generator and DC motor. (b) A 40 Hz transformer is to be used on a 50 Hz system. Assuming the Steinmetz's 07 coefficient as 1.6 and losses at lower frequency 1.2%, 0.7% and 0.5% for I2R. hystersis and eddy current respectively. Find (a) losses on 50 Hz for the same supply voltage and current (b) Output at 50 Hz for the same total losses as on 40 Hz Estimate the leakage reactance of concentric winding in core type transformers 07 Q.4clearly stating the assumptions used. (b) Calculate the diameter and length of armature for a 7.5 kW, 4 pole, 1000 r.p.m. 220 07 V shunt motor. Given: full load efficiency=0.83; maximum gap flux density=0.9 Wb/m ; specific electric loading=30,000 ampere conductors per meter ; field form factor 0.7. Assume that the maximum efficiency occurs at full load and field current is 2.5% of rated current. The pole face is square. (a) With the help of neat sketch, explain the effect of armature reaction on air gap flux 07 in case of DC machine. (b) A 4 pole generator supplies a current of 140 A. It has 480 armature conductors (a) 07 wave connected, (b) lap connected. The brushes are given an actual lead of 10'. Calculate the cross and demagnetizing minf per pole in each case. The field winding is shunt connected and takes a current of 10 A, find the number of extra shunt field turns to neutralize the demagnetization. (a) Describe the different methods adopted to reduce the effect of armature reaction in 07 Q.5 DC machine. The following particulars refer to the shunt field coil for a 440 V, 6 pole, DC 07 generator: Mmf per pole 7000 A; depth of winding 50mm; length of inner turn-1.1m; length of outer turn-1.4m, loss radiated from outer surface excluding ends = 1400 W/m<sup>2</sup>; space factor=0.62; resistivity= 0.02 Ω/m and mm2. Calculate (a) the diameter of wire (b) length of coil (c) number of turns and (d) exciting current. Assume a voltage drop of 20 % of terminal voltage across the field regulator. State the guiding factors while selecting the no. of armature slots in DC machine. O.5 (a) 07
  - (b) An 11 kV, 25 Hz transformer has I<sup>2</sup>R, hystersis and eddy current losses 1.6, 0.6 and 0.4 percent of the output. What will be the percentage losses if the transformer is connected to 22 kV, 50 Hz supply assuming the full load current to remain the same?

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**DJ Losen** 

BASICS OF ELECTRICAL ENGINEERING AND ELECTRONIC COMPONENTS K. Shashidhar, 2013-05-31 BASICS OF ELECTRICAL ENGINEERING AND ELECTRONIC COMPONENTS is intended to be used as a text book for I Semester Diploma in Electronics and Communication Engineering This book is designed for comprehensively covering all topics relevant to the subject Each and every topic has been explained in a very simple language as per the syllabus prescribed by the Board of Technical Education Karnataka This book is divided into eight chapters Chapter 1 Basics of Electricity Chapter 2 Electrostatics Chapter 3 Electromagnetic Induction Chapter 4 AC Fundamentals Chapter 5 AC Circuits Chapter 6 Transformers Chapter 7 Batteries Relays and Motors Chapter 8 Passive Components The text provides detailed explanations and uses numerous easy to follow examples accompanied by diagrams and step by step solutions Illustrative problems are presented in terms of commonly used voltages and current ratings To enhance the utility of the book important points and review questions objective and descriptive type have been included at the end of each chapter Model question papers have been provided to help students prepare better for the semester examinations Multiple choice questions along with answers have been given towards the end of the book for the benefit of students taking up competitive tests It is hoped that this book will be of immense use to teachers and students of Polytechnics Suggestions for improvement in the future editions of this book will be appreciated I wish to express my gratitude to MEI Polytechnic Bangalore for providing me an opportunity to bring out this text book I am grateful to Sri Nitin S Shah M s Sapna Book House Bangalore for publishing this book I am thankful to M s Datalink Bangalore for meticulous processing of the manuscript of this book BASIC ELECTRICAL ENGINEERING Dr. K. A. Navas, T. A. Suhail, 2016-08-01 This book is prepared as per the syllabus of VISVESVARAYA TECHNOLOGICAL UNIVERSITY Karnataka for first year B Tech Engineering course using the reference books given in the course syllabus Authors have tried to elucidate the topics such a way that even a mediocre student can assimilate them Many solved problems sample question papers and exercise given in every section will provide a thorough understanding of topics

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network analysis The book includes the discussion of network functions of one and two port networks The book incorporates the detailed discussion of resonant circuits The book covers the various aspects of two port network parameters along with the conditions of symmetry and reciprocity It also derives the interrelationships between the two port network parameters. The book uses plain and lucid language to explain each topic Each chapter gives the conceptual knowledge about the topic dividing it in various sections and subsections. The book provides the logical method of explaining the various complicated topics and stepwise methods to make the understanding easy. The variety of solved examples is the feature of this book. The book explains the philosophy of the subject which makes the understanding of the subject very clear and makes the subject more interesting.

Basics of Electrical Electronics and Communication Engineering Dr. K. A. Navas, T. A. Suhail, 2010-08-01. The book is written per the syllabus of first year engineering degree course for various universities. It covers basic topics of electrical electronics and communication engineering. It also includes worked out examples University examination questions and answers exercise etc in every chapter. This book is suitable for course in basic electrical and electronics engineering under various. Universities Authors have tried to elucidate the topics in such a way that even a mediocre student can assimilate them Many solved problems sample question papers and exercise given in every section will provide a thorough understanding of the topics. Other features include attractive writing style well structured equations and numerical examples pictures of high clarity etc. This book is one among prescribed textbooks for the syllabus of BIT Mesra Ranchi.

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