

RealView Assembler User's Guide

Instruction summary

Table 4.1 gives an overview of the instructions available in the ARM, Thumb, and ThumbEE instruction sets. Use it to locate individual instructions and pseudo-instructions described in the rest of this chapter.

Note

Unless stated otherwise, ThumbEE instructions are identical to Thumb instructions.

Table 4.1. Location of instructions

Mnemonic	Brief description	Page	Arch. [1]
<i>ADC, ADD</i>	Add with Carry, Add	<i>ADD, SUB, RSB, ADC, SBC, and RSC</i>	All
<i>ADR</i>	Load program or register-relative address (short range)	<i>ADR</i>	All
<i>ADRL</i> pseudo-instruction	Load program or register-relative address (medium range)	<i>ADRL pseudo-instruction</i>	×6M
<i>AND</i>	Logical AND	<i>AND, ORR, EOR, BIC, and ORN</i>	All
<i>ASR</i>	Arithmetic Shift Right	<i>ASR, LSL, LSR, ROR, and RRX</i>	All
<i>B</i>	Branch	<i>B, BL, BX, BLX, and BXJ</i>	All
<i>BFC, BFI</i>	Bit Field Clear and Insert	<i>BFC and BFI</i>	T2
<i>BIC</i>	Bit Clear	<i>AND, ORR, EOR, BIC, and ORN</i>	All
<i>BKPT</i>	Breakpoint	<i>BKPT</i>	5
<i>BL</i>	Branch with Link	<i>B, BL, BX, BLX, and BXJ</i>	All
<i>BLX</i>	Branch with Link, change instruction set	<i>B, BL, BX, BLX, and BXJ</i>	T
<i>BX</i>	Branch, change instruction set	<i>B, BL, BX, BLX, and BXJ</i>	T
<i>BXJ</i>	Branch, change to Jazelle	<i>B, BL, BX, BLX, and BXJ</i>	J, ×7M
<i>CBZ, CBNZ</i>	Compare and Branch if {Non}Zero	<i>CBZ and CBNZ</i>	T2
<i>CDP</i>	Coprocessor Data Processing operation	<i>CDP and CDP2</i>	×6M
<i>CDPC</i>	Coprocessor Data Processing operation	<i>CDP and CDP2</i>	5, ×6M
<i>CHKA</i>	Check: array	<i>CHKA</i>	EE

Realview Assembler Users Guide

Yan Bai



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