An Introduction to Parallel Programming Solutions, Chapter 1

Jinyoung Choi and Peter Pacheco

February 1, 2011

2. We are assigning blocks of elements to cores in order (the first n/p elements to core 0, the next n/p elements to core 1, so on). So, for example, if n = 12 and p = 4, core 0 spends 12 milliseconds in the call to Compute_next_value (i = 0, 1, 2), core 1 spends 30 milliseconds (i = 3, 4, 5), core 2 spends 48 milliseconds (i = 6, 7, 8), and core 3 spends 66 milliseconds (i = 9, 10, 11). So clearly this assignment will do a very poor job of load balancing.

A better approach uses a cyclic assignment of the work to the cores:

```
/* assign[c][j] is the jth value of i assigned to core c */
/* work[c] is the total amount of work assigned to core c */
c = j = 0;
for (i = 0; i < n; i++) {
    work[c] += 2*(i+1);
    assign[c][j] = i;
    c = (c + 1) % p;
    if (c == 0) j++;
}</pre>
```

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Solutions Manual to Scientific Parallel Computing L. R. Scott, 2005-01-05 **Instructor's Solutions Manual to** Accompany Scaladle Parallel Computing, Technology, Architecture and Programming [by] Kai Hwang, Zhiwei Xu Kwai Hwang, Zhiwei Xu, 1998 **Introduction to Parallel Computing** Vipin Kumar, 2001-07-01 **Handbook of Parallel Computing** Sanguthevar Rajasekaran, John Reif, 2007-12-20 The ability of parallel computing to process large data sets and handle time consuming operations has resulted in unprecedented advances in biological and scientific computing modeling and simulations Exploring these recent developments the Handbook of Parallel Computing Models Algorithms and Applications provides comprehensive coverage on a An Introduction to Parallel Programming Peter Pacheco, Matthew Malensek, 2021-08-27 An Introduction to Parallel Programming Second Edition presents a tried and true tutorial approach that shows students how to develop effective parallel programs with MPI Pthreads and OpenMP As the first undergraduate text to directly address compiling and running parallel programs on multi core and cluster architecture this second edition carries forward its clear explanations for designing debugging and evaluating the performance of distributed and shared memory programs while adding coverage of accelerators via new content on GPU programming and heterogeneous programming New and improved user friendly exercises teach students how to compile run and modify example programs Takes a tutorial approach starting with small programming examples and building progressively to more challenging examples Explains how to develop parallel programs using MPI Pthreads and OpenMP programming models A robust package of online ancillaries for instructors and students includes lecture slides solutions manual downloadable source code and an image bank New to this edition New chapters on GPU programming and heterogeneous programming New examples and exercises related to parallel algorithms **OpenMP Shared Memory Parallel Programming Rudolf** Eigenmann, Michael J. Voss, 2003-05-15 This book contains the presentations given at the Workshop on OpenMP App cations and Tools WOMPAT 2001 The workshop was held on July 30 and 31 2001 at Purdue University West Lafayette Indiana USA It brought together designers users and researchers of the OpenMP application programming int face OpenMP has emerged as the standard for shared memory parallel programing For the rst time it is possible to write parallel programs that are portable across the majority of shared memory parallel computers WOMPAT 2001 s ved as a forum for all those interested in OpenMP and allowed them to meet share ideas and experiences and discuss the latest developments of OpenMP and its applications WOMPAT 2001 was co sponsored by the OpenMP Architecture Review Board ARB It followed a series of workshops on OpenMP including WOMPAT 2000 EWOMP 2000 and WOMPEI 2000 For WOMPAT 2001 we solicited papers formally and published them in the form of this book The authors submitted extended abstracts which were reviewed by the program committee All submitted papers were accepted The authors were asked to prepare a nal paper in which they addressed the reviewers comments The proceedings in the form of this book were created in time to be available at the

workshop In this way we hope to have brought out a timely report of ongoing OpenMP related research and development e orts as well as ideas for future improvements **Parallel Computing Technologies** Victor Malyshkin, 2023-08-14 This book constitutes the refereed proceedings of the 17th International Conference on Parallel Computing Technologies PaCT 2023 held in Astana Kazakhstan during August 21 25 2023 The 15 full papers included in this book were carefully reviewed and selected from 23 submissions They were organized in topical sections as follows automatic programming and program tuning frameworks and services algorithms and distributed systems management Parallel Programming Thomas Rauber, Gudula Rünger, 2013-06-13 Innovations in hardware architecture like hyper threading or multicore processors mean that parallel computing resources are available for inexpensive desktop computers In only a few years many standard software products will be based on concepts of parallel programming implemented on such hardware and the range of applications will be much broader than that of scientific computing up to now the main application area for parallel computing Rauber and R nger take up these recent developments in processor architecture by giving detailed descriptions of parallel programming techniques that are necessary for developing efficient programs for multicore processors as well as for parallel cluster systems and supercomputers Their book is structured in three main parts covering all areas of parallel computing the architecture of parallel systems parallel programming models and environments and the implementation of efficient application algorithms The emphasis lies on parallel programming techniques needed for different architectures For this second edition all chapters have been carefully revised The chapter on architecture of parallel systems has been updated considerably with a greater emphasis on the architecture of multicore systems and adding new material on the latest developments in computer architecture Lastly a completely new chapter on general purpose GPUs and the corresponding programming techniques has been added The main goal of the book is to present parallel programming techniques that can be used in many situations for a broad range of application areas and which enable the reader to develop correct and efficient parallel programs Many examples and exercises are provided to show how to apply the techniques The book can be used as both a textbook for students and a reference book for professionals The material presented has been used for courses in parallel programming at different universities for manyyears Parallel Programming Bertil Schmidt, Jorge Gonzalez-Martinez, Christian Hundt, Moritz Schlarb, 2017-11-20 Parallel Programming Concepts and Practice provides an upper level introduction to parallel programming In addition to covering general parallelism concepts this text teaches practical programming skills for both shared memory and distributed memory architectures The authors open source system for automated code evaluation provides easy access to parallel computing resources making the book particularly suitable for classroom settings Covers parallel programming approaches for single computer nodes and HPC clusters OpenMP multithreading SIMD vectorization MPI UPC Contains numerous practical parallel programming exercises Includes access to an automated code evaluation tool that enables students the opportunity to program in a web browser and receive immediate

feedback on the result validity of their program Features an example based teaching of concept to enhance learning **High-Level Parallel Programming Models and Supportive Environments** Frank Mueller, 2003-05-15 On outcomes the 23rd of April 2001 the 6th Workshop on High Level Parallel P gramming Models and Supportive Environments LCTES 98 was held in San Francisco HIPShas been held over the past six years in conjunction with IPDPS the Internation Parallel and Distributed Processing Symposium The HIPSworkshop focuses on high level programming of networks of wo stations computing clusters and of massively parallel machines Its goal is to bring together researchers working in the areas of applications language design compilers system architecture and programming tools to discuss new devel ments in programming such systems In recent years several standards have emerged with an increasing demand of support for parallel and distributed processing On one end message passing frameworks such as PVM MPI and VIA provide support for basic commu cation On the other hand distributed object standards such as CORBA and DCOM provide support for handling remote objects in a client server fashion but also ensure certain quarantees for the quality of services The key issues for the success of programming parallel and distributed en ronments are high level programming concepts and e ciency In addition other quality categories have to be taken into account such as scalability security bandwidth guarantees and fault tolerance just to name a few Today's challenge is to provide high level programming concepts without s ri cing e ciency This is only possible by carefully designing for those concepts and by providing supportive programming environments that facilitate **OpenMP Shared Memory Parallel Programming Michael J. Voss, 2007-03-05 The** program development and tuning refereed proceedings of the International Workshop on OpenMP Applications and Tools WOMPAT 2003 held in Toronto Canada in June 2003 The 20 revised full papers presented were carefully reviewed and selected for inclusion in the book The papers are organized in sections on tools and tool technology OpenMP implementations OpenMP experience and OpenMP on clusters <u>Introduction to Parallel Computing</u> Wesley P. Petersen, Peter Arbenz, 2004 This is a practical student guide to scientific computing on parallel computers working up from a hardware instruction level to shared memory machines and finally to distributed memory machines Algorithms and Architectures for Parallel Processing Tianging Zhu, Jin Li, Aniello Castiglione, 2025-02-12 The six volume set LNCS 15251 15256 constitutes the refereed proceedings of the 24th International Conference on Algorithms and Architectures for Parallel Processing ICA3PP 2024 held in Macau China during October 29 31 2024 The 91 full papers 35 short papers and 5 workshop papers included in these proceedings were carefully reviewed and selected from 265 submissions They focus on the many dimensions of parallel algorithms and architectures encompassing fundamental theoretical approaches practical experimental projects and commercial components and systems **Analysis** and Design of Parallel Algorithms S. Lakshmiv Arahan,1989-09-01 **Languages and Compilers for Parallel Computing** Xipeng Shen, Frank Mueller, James Tuck, 2016-02-19 This book constitutes the thoroughly refereed post conference proceedings of the 28th International Workshop on Languages and Compilers for Parallel Computing LCPC 2015 held in

Raleigh NC USA in September 2015 The 19 revised full papers were carefully reviewed and selected from 44 submissions The papers are organized in topical sections on programming models optimizing framework parallelizing compiler communication and locality parallel applications and data structures and correctness and reliability Parallel Computing David Padua, 2014-07-08 Containing over 300 entries in an AZ format the Encyclopedia of Parallel Computing provides easy intuitive access to relevant information for professionals and researchers seeking access to any aspect within the broad field of parallel computing Topics for this comprehensive reference were selected written and peer reviewed by an international pool of distinguished researchers in the field The Encyclopedia is broad in scope covering machine organization programming languages algorithms and applications Within each area concepts designs and specific implementations are presented. The highly structured essays in this work comprise synonyms a definition and discussion of the topic bibliographies and links to related literature Extensive cross references to other entries within the Encyclopedia support efficient user friendly searchers for immediate access to useful information Key concepts presented in the Encyclopedia of Parallel Computing include laws and metrics specific numerical and non numerical algorithms asynchronous algorithms libraries of subroutines benchmark suites applications sequential consistency and cache coherency machine classes such as clusters shared memory multiprocessors special purpose machines and dataflow machines specific machines such as Cray supercomputers IBM s cell processor and Intel s multicore machines race detection and auto parallelization parallel programming languages synchronization primitives collective operations message passing libraries checkpointing and operating systems Topics covered Speedup Efficiency Isoefficiency Redundancy Amdahls law Computer Architecture Concepts Parallel Machine Designs Benmarks Parallel Programming concepts design Algorithms Parallel applications This authoritative reference will be published in two formats print and online The online edition features hyperlinks to cross references and to additional significant research Related Subjects supercomputing high performance computing distributed Scientific and Technical Aerospace Reports ,1995 Custom Memory Management Methodology computing Francky Catthoor, Sven Wuytack, G.E. de Greef, Florin Banica, Lode Nachtergaele, Arnout Vandecappelle, 2013-03-09 The main intention of this book is to give an impression of the state of the art in system level memory management data transfer and storage related issues for complex data dominated real time signal and data processing applications The material is based on research at IMEC in this area in the period 1989 1997 In order to deal with the stringent timing requirements and the data dominated characteristics of this domain we have adopted a target architecture style and a systematic methodology to make the exploration and optimization of such systems feasible Our approach is also very heavily application driven which is illustrated by several realistic demonstrators partly used as red thread examples in the book Moreover the book addresses only the steps above the traditional high level synthesis scheduling and allocation or compilation traditional or ILP oriented tasks The latter are mainly focussed on scalar or scalar stream operations and data where the internal structure of the

complex data types is not exploited in contrast to the approaches discussed here The proposed methodologies are largely independent of the level of programmability in the data path and controller so they are valuable for the realisation of both hardware and software systems Our target domain consists of signal and data processing systems which deal with large **Logic Programming** I. Balbin, K. Lecot, 2012-12-06 Logic Programming was effectively defined as a amounts of data discipline in the early seventies It is only during the early to mid eighties that books conferences and journals devoted entirely to Logic Programming began to appear Consequently much of the work done during this first crucial decade in Marseilles Edinburgh London Budapest and Stockholm to name a few is often overlooked or difficult to trace There are now two main regular conferences on Logic Programming and at least five journals The Journal of Logic Programming New Generation Computing Automated Reasoning The Journal of Slmbolic Computation and Future Generation Computer Systems Logic Programming however has its roots in Automated Theorem Proving and via the expanding area of expert systems strongly influences researchers in such varied fields as Civil Engineering Chemistry Law etc Consequently many papers related to Logic Programming appear in a wide variety of journals and proceedings of conferences in other disciplines This is particularly true of Computer Science where a revolution is taking place in hardware design programming languages and more recently databases One cannot overestimate the importance of such a bibliography Monthly Catalogue, United States Public Documents, 1995

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