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Quantum Computing for Computer Architects

Tzvetan Metodi
Frederic T. Chong

*SYNTHESIS LECTURES ON
COMPUTER ARCHITECTURE*

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Quantum Computing For Computer Architects Frederic T Chong

**Lizhong Chen, Drew Penney, Daniel
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Quantum Computing For Computer Architects Frederic T Chong:

Quantum Computing for Computer Architects Tzvetan S. Metodi, Arvin I. Faruque, Frederic T. Chong, 2011 Quantum computation may seem to be a topic for science fiction but small quantum computers have existed for several years and larger machines are on the drawing table. These efforts have been fueled by a tantalizing property while conventional computers employ a binary representation that allows computational power to scale linearly with resources at best quantum computations employ quantum phenomena that can interact to allow computational power that is exponential in the number of quantum bits in the system. Quantum devices rely on the ability to control and manipulate binary data stored in the phase information of quantum wave functions that describe the electronic states of individual atoms or the polarization states of photons. While existing quantum technologies are in their infancy we shall see that it is not too early to consider scalability and reliability. In fact such considerations are a critical link in the development chain of viable device technologies capable of orchestrating reliable control of tens of millions quantum bits in a large scale system. The goal of this lecture is to provide architectural abstractions common to potential technologies and explore the systems level challenges in achieving scalable fault tolerant quantum computation.

Quantum Computing for Computer Architects Tzvetan S. Metodi, 2006

Quantum Computing for Computer Architects, Second Edition Tzvetan Metodi, Arvin I. Faruque, 2022-06-01 Quantum computers can in theory solve certain problems far faster than a classical computer running any known classical algorithm. While existing technologies for building quantum computers are in their infancy it is not too early to consider their scalability and reliability in the context of the design of large scale quantum computers. To architect such systems one must understand what it takes to design and model a balanced fault tolerant quantum computer architecture. The goal of this lecture is to provide architectural abstractions for the design of a quantum computer and to explore the systems level challenges in achieving scalable fault tolerant quantum computation. In this lecture we provide an engineering oriented introduction to quantum computation with an overview of the theory behind key quantum algorithms. Next we look at architectural case studies based upon experimental data and future projections for quantum computation implemented using trapped ions. While we focus here on architectures targeted for realization using trapped ions the techniques for quantum computer architecture design quantum fault tolerance and compilation described in this lecture are applicable to many other physical technologies that may be viable candidates for building a large scale quantum computing system. We also discuss general issues involved with programming a quantum computer as well as a discussion of work on quantum architectures based on quantum teleportation. Finally we consider some of the open issues remaining in the design of quantum computers.

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Synthesis Lectures on Computer Architecture : Quantum

Computing for Computer Architects (2nd Edition) Tzvetan S. Metodi, *Quantum Computer Systems* Yongshan Ding, 2020-06-17 This book targets computer scientists and engineers who are familiar with concepts in classical computer systems but are curious to learn the general architecture of quantum computing systems It gives a concise presentation of this new paradigm of computing from a computer systems point of view without assuming any background in quantum mechanics As such it is divided into two parts The first part of the book provides a gentle overview on the fundamental principles of the quantum theory and their implications for computing The second part is devoted to state of the art research in designing practical quantum programs building a scalable software systems stack and controlling quantum hardware components Most chapters end with a summary and an outlook for future directions This book celebrates the remarkable progress that scientists across disciplines have made in the past decades and reveals what roles computer scientists and engineers can play to enable practical scale quantum computing

Deep Learning for Computer Architects Brandon Reagen, Robert Adolf, Paul Whatmough, Gu-Yeon Wei, David Brooks, 2022-05-31 Machine learning and specifically deep learning has been hugely disruptive in many fields of computer science The success of deep learning techniques in solving notoriously difficult classification and regression problems has resulted in their rapid adoption in solving real world problems The emergence of deep learning is widely attributed to a virtuous cycle whereby fundamental advancements in training deeper models were enabled by the availability of massive datasets and high performance computer hardware This text serves as a primer for computer architects in a new and rapidly evolving field We review how machine learning has evolved since its inception in the 1960s and track the key developments leading up to the emergence of the powerful deep learning techniques that emerged in the last decade Next we review representative workloads including the most commonly used datasets and seminal networks across a variety of domains In addition to discussing the workloads themselves we also detail the most popular deep learning tools and show how aspiring practitioners can use the tools with the workloads to characterize and optimize DNNs The remainder of the book is dedicated to the design and optimization of hardware and architectures for machine learning As high performance hardware was so instrumental in the success of machine learning becoming a practical solution this chapter recounts a variety of optimizations proposed recently to further improve future designs Finally we present a review of recent research published in the area as well as a taxonomy to help readers understand how various contributions fall in context

Computer Architecture Techniques for Power-Efficiency Stefanos Kaxiras, Margaret Martonosi, 2022-06-01 In the last few years power dissipation has become an important design constraint on par with performance in the design of new computer systems Whereas in the past the primary job of the computer architect was to translate improvements in operating frequency and transistor count into performance now power efficiency must be taken into account at every step of the design process While for some time architects have been successful in

delivering 40% to 50% annual improvement in processor performance costs that were previously brushed aside eventually caught up. The most critical of these costs is the inexorable increase in power dissipation and power density in processors. Power dissipation issues have catalyzed new topic areas in computer architecture resulting in a substantial body of work on more power efficient architectures. Power dissipation coupled with diminishing performance gains was also the main cause for the switch from single core to multi core architectures and a slowdown in frequency increase. This book aims to document some of the most important architectural techniques that were invented, proposed and applied to reduce both dynamic power and static power dissipation in processors and memory hierarchies. A significant number of techniques have been proposed for a wide range of situations and this book synthesizes those techniques by focusing on their common characteristics.

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FPGA-Accelerated Simulation of Computer Systems Hari Angepat, Derek Chiou, Eric S. Chung, James C. Hoe, 2022-05-31 To date the most common form of simulators of computer systems are software based running on standard computers. One promising approach to improve simulation performance is to apply hardware specifically reconfigurable hardware in the form of field programmable gate arrays (FPGAs). This manuscript describes various approaches of using FPGAs to accelerate software implemented simulation of computer systems and selected simulators that incorporate those techniques. More precisely we describe a simulation architecture taxonomy that incorporates a simulation architecture specifically designed for FPGA accelerated simulation, survey the state of the art in FPGA accelerated simulation and describe in detail selected instances of the described techniques.

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AI for Computer Architecture Lizhong Chen, Drew Penney, Daniel Jiménez, 2022-05-31 Artificial intelligence has already enabled pivotal advances in diverse fields yet its impact on computer architecture has only just begun. In particular recent work has explored broader application to the design optimization and simulation of computer architecture. Notably machine learning based strategies often surpass prior state of the art analytical heuristic and human expert approaches. This book reviews the application of machine learning in system wide simulation and run time optimization and in many individual components such as caches, memories, branch predictors, networks on chip and GPUs. The book further analyzes current practice to highlight useful design strategies and identify areas for future work based on optimized implementation strategies, opportune extensions to existing work and ambitious long term possibilities. Taken together these strategies and techniques present a promising future for increasingly automated computer architecture designs.

Optimization and Mathematical Modeling in Computer Architecture Karthikeyan Sankaralingam, Michael Ferris, Tony Nowatzki, Cristian Estan, Nilay Vaish, David Wood, 2022-05-31 In this book we give an

overview of modeling techniques used to describe computer systems to mathematical optimization tools We give a brief introduction to various classes of mathematical optimization frameworks with special focus on mixed integer linear programming which provides a good balance between solver time and expressiveness We present four detailed case studies instruction set customization data center resource management spatial architecture scheduling and resource allocation in tiled architectures showing how MILP can be used and quantifying by how much it outperforms traditional design exploration techniques This book should help a skilled systems designer to learn techniques for using MILP in their problems and the skilled optimization expert to understand the types of computer systems problems that MILP can be applied to

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