



Government Girls' Polytechnic, Bilaspur

Name of the Lab: Digital Electronics Lab

Practical: Digital Electronics Lab

Class :3rd Semester (ET&T), 3rd Semester (CS)
FOR ET&T

Teachers Assessment: 10 End Semester Examination: 50

FOR CS

Teachers Assessment: 20 End Semester Examination: 50

EXPERIMENT No- 1

OBJECTIVE: - Verify Truth Table of Logic Gates (AND, OR, NOT, NAND & NOR Gates).

EQUIPMENT REQUIRED: - All the basic gate mention in the fig.

THEORY: - There are different gates available in digital electronic filed. Which is used to perform the different tasks they are as follow:-

AND GATE: - This gate is used for the multiplication of two binary digits.

OR GATE: - This gate is used for the addition of two binary digits.

NOT GATE: - This gate gives the compliment of given binary digit.

NAND GATE: - This gate is used for giving compliment of multiplication of two binary digits.

NOR GATE: - This gate is used for giving compliment of the addition of two binary digits

DAIGRAMS:-

2-input AND gate



A	B	Output
0	0	0
0	1	0
1	0	0
1	1	1

1-input NOT gate

2-input OR gate



A	B	Output
0	0	0
0	1	1
1	0	1
1	1	1

2-input NAND gate

Polytechnic Electronics Lab Manual

ML Morrison



Polytechnic Electronics Lab Manual:

Whispering the Strategies of Language: An Psychological Journey through **Polytechnic Electronics Lab Manual**

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Table of Contents Polytechnic Electronics Lab Manual

1. Understanding the eBook Polytechnic Electronics Lab Manual
 - The Rise of Digital Reading Polytechnic Electronics Lab Manual
 - Advantages of eBooks Over Traditional Books
2. Identifying Polytechnic Electronics Lab Manual
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Polytechnic Electronics Lab Manual
 - User-Friendly Interface
4. Exploring eBook Recommendations from Polytechnic Electronics Lab Manual
 - Personalized Recommendations

- Polytechnic Electronics Lab Manual User Reviews and Ratings
- Polytechnic Electronics Lab Manual and Bestseller Lists
- 5. Accessing Polytechnic Electronics Lab Manual Free and Paid eBooks
 - Polytechnic Electronics Lab Manual Public Domain eBooks
 - Polytechnic Electronics Lab Manual eBook Subscription Services
 - Polytechnic Electronics Lab Manual Budget-Friendly Options
- 6. Navigating Polytechnic Electronics Lab Manual eBook Formats
 - ePub, PDF, MOBI, and More
 - Polytechnic Electronics Lab Manual Compatibility with Devices
 - Polytechnic Electronics Lab Manual Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Polytechnic Electronics Lab Manual
 - Highlighting and Note-Taking Polytechnic Electronics Lab Manual
 - Interactive Elements Polytechnic Electronics Lab Manual
- 8. Staying Engaged with Polytechnic Electronics Lab Manual
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Polytechnic Electronics Lab Manual
- 9. Balancing eBooks and Physical Books Polytechnic Electronics Lab Manual
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Polytechnic Electronics Lab Manual
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Polytechnic Electronics Lab Manual
 - Setting Reading Goals Polytechnic Electronics Lab Manual
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Polytechnic Electronics Lab Manual
 - Fact-Checking eBook Content of Polytechnic Electronics Lab Manual

- Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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