

M.Tech. Degree Examination, June/July 2011
Real Time Operating Systems

Time: 3 hrs.

Max. Marks:100

Note: Answer any FIVE full questions.

1.
 - a. With diagrams explain Hard real time, Isochronal soft real-time and Soft isochronal service utilities. (08 Marks)
 - b. Differentiate between preemptive and non-preemptive scheduling. (06 Marks)
 - c. Explain thread safe reentrant function. (06 Marks)
2.
 - a. Explain RM scheduling. Calculate the "utility of the CPU resource achievable" for the following system of tasks.
 i) $T_1 = 2$, $T_2 = 5$, $C_1 = 1$, $C_2 = 1$
 ii) $T_1 = 2$, $T_2 = 5$, $C_1 = 1$, $C_2 = 2$
 Write scheduling diagram for each of the above. (08 Marks)
 - b. With reference to RM LUB differentiate between i) sufficient ii) necessary and sufficient conditions for feasibility tests. (06 Marks)
 - c. With examples differentiate between deadline monotonic and earliest deadline first policies. (06 Marks)
3.
 - a. Encode the following data using ECC/Hamming coding.
 "1 1 0 0 0 1 0 0"
 Prepare the table of coding. (08 Marks)
 - b. Explain a simple pipeline with an example of stage overlap depth = 4. (06 Marks)
 - c. With diagram explain the three types of cache organizations. (06 Marks)
4.
 - a. Write scheduling diagram for the following tasks to illustrate "DM policy succeeds when RM would fail".
 $T_1 = 2$; $C_1 = 1$; $D_1 = 2$
 $T_2 = 5$; $C_2 = 1$; $D_2 = 3$
 $T_3 = 2$; $C_3 = 1$; $D_3 = 7$
 $T_4 = 13$; $C_4 = 2$; $D_4 = 15$ (08 Marks)
 - b. Explain the following :
 i) Deadlock ii) Critical section iii) Priority inversion. (06 Marks)
 - c. Explain availability and quality of service. (06 Marks)
5.
 - a. With a block diagram, explain different subsystems for stereo-vision tracking system. (08 Marks)
 - b. Explain boot code and device drivers. (06 Marks)
 - c. Explain binary and mutex semaphores. (06 Marks)
6.
 - a. What are exceptions, assert and single step debugging? How are they used? (08 Marks)
 - b. Differentiate between hardware and software breakpoints. (06 Marks)
 - c. Explain test access ports and trace ports. (06 Marks)
7.
 - a. Explain reliability of dual string cross-strapped subsystem interconnection. (08 Marks)
 - b. With the help of a block diagram, explain low-end PIC micro MCU programmer. (06 Marks)
 - c. Write a note on PIC 17CXX programming. (06 Marks)
8.
 - a. Write a note on real time operating system. (06 Marks)
 - b. Devise a strategy to derive the RM CUB for any given set of services S for which each services S_i has an arbitrary C_i , T_i for the two cases.
 i) C_i short enough to fit all the releases in T_i . ii) C_i too large to fit last release T_i . (14 Marks)

Important Note : 1. On completing your answers, compulsorily draw diagonal cross lines on the remaining blank pages.
 2. Any revealing of identification, appeal to evaluator and/or equations written eg. $4 \times 4 = 16$, will be treated as malpractice.

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