COMP2121 Sample Exam Questions

| 1.7 | Convert the | following: | numbers i | feored the | e original. | busse to | o tibe si | pecified? | business |
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| ar) | 123:0 | · · · · · · · · · · · · · · · · · · · | |
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| - | and the second second | | |

- What is the result of the following calculations?
 - a) 1395 + 4988 (base 16)
 - b) 11001001 + 00101101 (base 2)
 - c) A41 560 (base 16)
 - d) 11001 011 (base 2)
- 4. What number does 10010010 represent as an unsigned number? What does it represent in 2's complement notation?
- In 2's complement addition, 11011011 + 01100000 = 00111011. Was there a 2's complement overflow? Why? What do the values in this sum represent?
- 6. What is the difference between performing 2's complement addition and unsigned addition in the AVR processor?
- Represent the following numbers in IEEE 754 32-bit floating point notation:
 - a) = 1..5
 - b) 1084
 - c) = -1
 - d) -13.75
- 8. What does the following IEEE 754 FP number represent:

| 0 | 1000 0001 | 110 0000 0000 0000 0000 0000 |
|------|-----------|------------------------------|
| Sign | Exponent | Mantissa |

Sample Exam Questions On Computer Hardware

L Reisser

Sample Exam Questions On Computer Hardware:

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